

BSX 28

HIGH-SPEED SATURATED SWITCH

NPN DIFFUSED SILICON PLANAR EPITAXIAL TRANSISTOR

GENERAL DESCRIPTION-The BSX 28 is an NPN silicon PLANAR epitaxial transistor designed specifically for high-speed saturated switching applications in the 50-100 Mc/s range at current levels from 100 microamperes to 100 milliamperes. It is suitable for most small-signal, RF, and digital type circuits.

ABSOLUTE MAXIMUM RATINGS (Note 1)

Maximum Temperatures

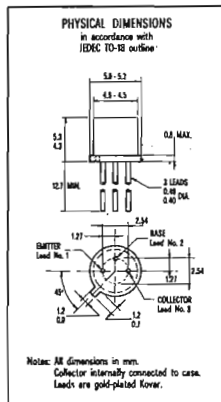
Storage Temperature	-55°C to + 200°C
Operating Junction Temperature	200°C Maximum
Lead Temperature (Soldering, 60 sec. time limit)	300°C Maximum

Maximum Power Dissipations

Total Dissipation at 25°C Case Temperature (Notes 2 and 3)	1.2 Watts
at 100°C Case Temperature (Notes 2 and 3)	0.68 Watt
at 25°C Ambient Temperature (Notes 2 and 3)	0.36 Watt

Maximum Voltages

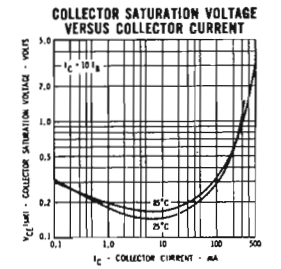
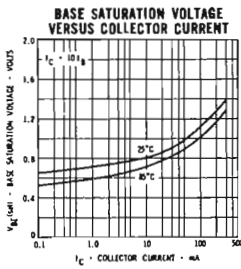
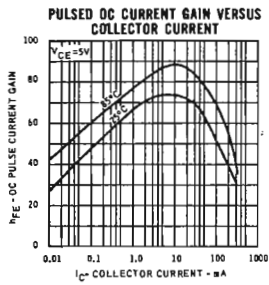
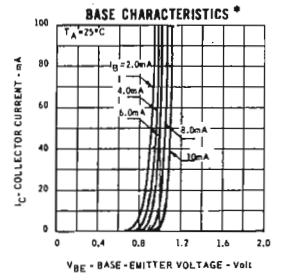
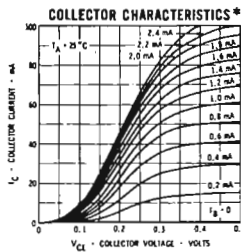
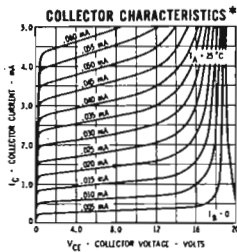
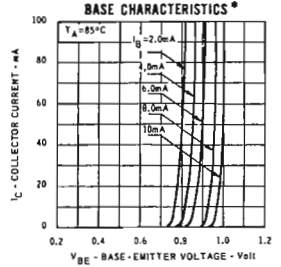
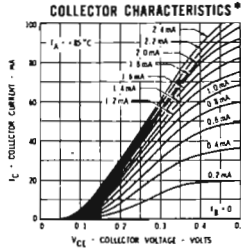
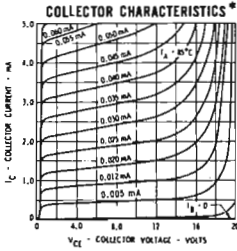
V _{CB0} Collector to Base Voltage	30 Volts
V _{CES} Collector to Emitter Voltage	30 Volts
V _{CEO} Collector to Emitter Voltage (Note 4)	12 Volts
V _{EBO} Emitter to Base Voltage	4.5 Volts



ELECTRICAL CHARACTERISTICS (25°C free air temperature unless otherwise noted)

SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNITS	TEST CONDITIONS
h_{FE}	DC Pulse Current Gain (Note 5)	30	70	120		$I_C = 10 \text{ mA}$ $V_{CE} = 0.35 \text{ V}$
h_{FE}	DC Pulse Current Gain (Note 5)	25	70			$I_C = 30 \text{ mA}$ $V_{CE} = 0.4 \text{ V}$
h_{FE}	DC Pulse Current Gain (Note 5)	12	50			$I_C = 100 \text{ mA}$ $V_{CE} = 1.0 \text{ V}$
$V_{BE}(\text{sat})$	Base Saturation Voltage	0.72	0.8	0.87	V	$I_C = 10 \text{ mA}$ $I_B = 1.0 \text{ mA}$
$V_{BE}(\text{sat})$	Base Saturation Voltage		0.9	1.15	V	$I_C = 30 \text{ mA}$ $I_B = 3.0 \text{ mA}$
$V_{BE}(\text{sat})$	Base Saturation Voltage		1.1	1.6	V	$I_C = 100 \text{ mA}$ $I_B = 10 \text{ mA}$
$V_{CE}(\text{sat})$	Collector Saturation Voltage		0.15	0.2	V	$I_C = 10 \text{ mA}$ $I_B = 1.0 \text{ mA}$
$V_{CE}(\text{sat})$	Collector Saturation Voltage		0.18	0.25	V	$I_C = 30 \text{ mA}$ $I_B = 3.0 \text{ mA}$
$V_{CE}(\text{sat})$	Collector Saturation Voltage		0.3	0.5	V	$I_C = 100 \text{ mA}$ $I_B = 10 \text{ mA}$
$V_{CE}(\text{sat})(85^\circ\text{C})$	Collector Saturation Voltage		0.17	0.3	V	$I_C = 10 \text{ mA}$ $I_B = 1.0 \text{ mA}$
I_{CES}	Collector Reverse Current		0.05	0.4	μA	$V_{CE} = 20 \text{ V}$ $V_{BE} = 0$
$I_{CES}(85^\circ\text{C})$	Collector Reverse Current		1.0	10	μA	$V_{CE} = 20 \text{ V}$ $V_{BE} = 0$
BV_{CB0}	Collector to Base Breakdown Voltage	30			V	$I_C = 10 \mu\text{A}$ $I_E = 0$
BV_{CES}	Collector to Emitter Breakdown Voltage	30			V	$I_C = 10 \mu\text{A}$ $V_{EB} = 0$
BV_{EBO}	Emitter to Base Breakdown Voltage	4.5			V	$I_E = 100 \mu\text{A}$ $I_C = 0$
$V_{CEO}(\text{sust})$	Collector to Emitter Sustaining Voltage (Note 5)	12			V	$I_C = 10 \text{ mA}$ $I_B = 0$ (pulsed)
h_{fe}	High Frequency Current Gain ($f = 100 \text{ Mc/s}$)	4.0	6.5			$I_C = 20 \text{ mA}$ $V_{CE} = 10 \text{ V}$
C_{ob}	Output Capacitance		2.3	4.0	pF	$I_E = 0$ $V_{CB} = 5.0 \text{ V}$
τ_s	Charge Storage Time Constant (Note 6)		6.5	13	nsec	$I_C = I_{B1} = 10 \text{ mA}$, $I_{B2} = -10 \text{ mA}$
t_{on}	Turn On Time (Note 6)		9.0	15	nsec	$I_C = 30 \text{ mA}$ $I_{B1} = 3.0 \text{ mA}$
t_{off}	Turn Off Time (Note 6)		13	20	nsec	$I_C = 30 \text{ mA}$, $I_{B1} = 3.0 \text{ mA}$, $I_{B2} = -3.0 \text{ mA}$

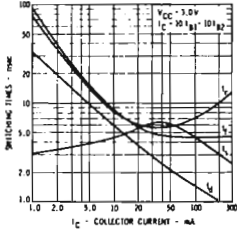
TYPICAL ELECTRICAL CHARACTERISTICS



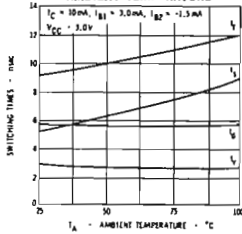
* Single family characteristics on Transistor Curve Tracer.

TYPICAL ELECTRICAL CHARACTERISTICS

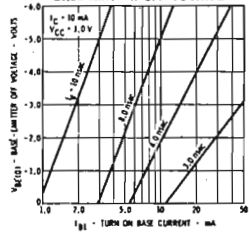
SWITCHING TIMES VERSUS COLLECTOR CURRENT



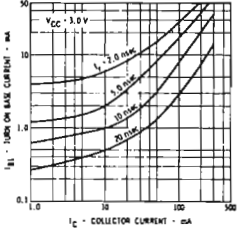
SWITCHING TIMES VERSUS AMBIENT TEMPERATURE



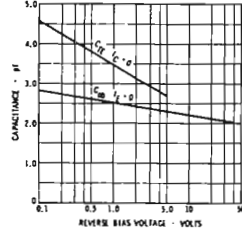
DELAY TIME VERSUS TURN ON BASE CURRENT AND BASE-EMITTER OFF VOLTAGE



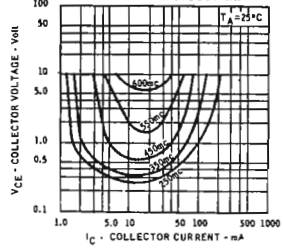
RISE TIME VERSUS COLLECTOR AND TURN ON BASE CURRENT



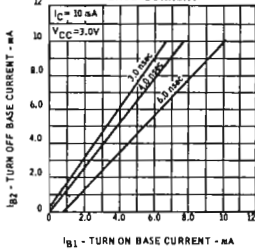
EMITTER TRANSITION AND OUTPUT CAPACITANCE VERSUS REVERSE BIAS VOLTAGE



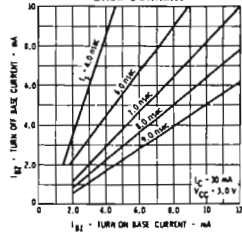
CONTOURS OF CONSTANT GAIN BANDWIDTH PRODUCT (f_T)



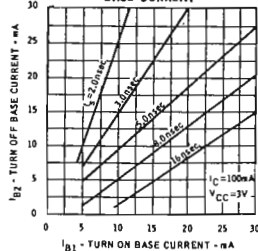
STORAGE TIME VERSUS TURN ON AND TURN OFF BASE CURRENT



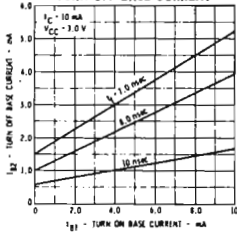
STORAGE TIME VERSUS TURN ON AND TURN OFF BASE CURRENT



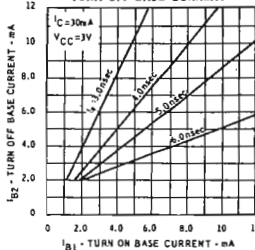
STORAGE TIME VERSUS TURN ON AND TURN OFF BASE CURRENT



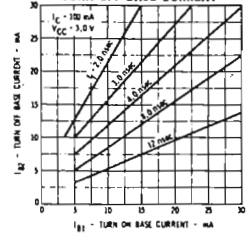
FALL TIME VERSUS TURN ON AND TURN OFF BASE CURRENT

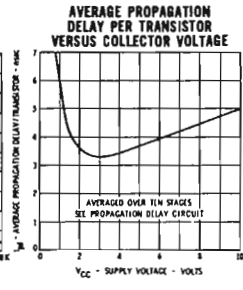
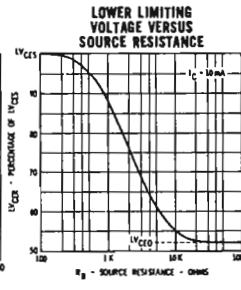
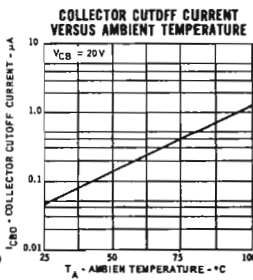
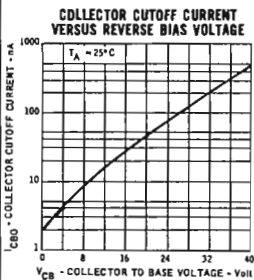


FALL TIME VERSUS TURN ON AND TURN OFF BASE CURRENT

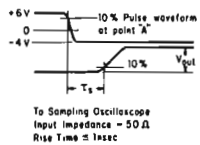
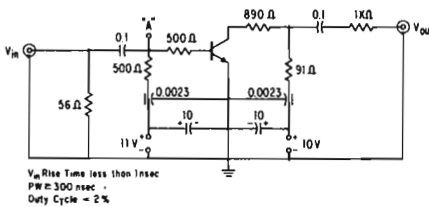
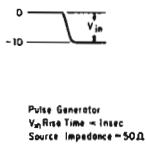


FALL TIME VERSUS TURN ON AND TURN OFF BASE CURRENT

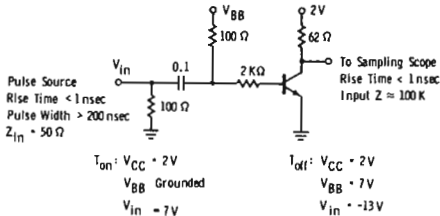




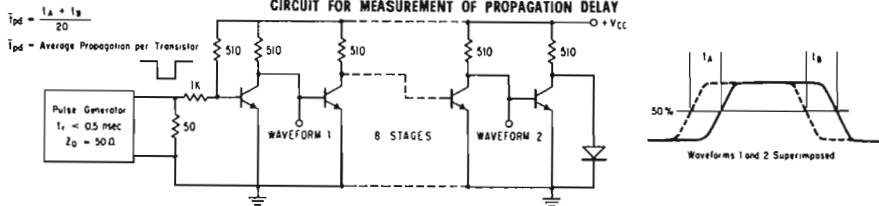
CHARGE STORAGE TIME — CONSTANT TEST CIRCUIT



$t_{on} - t_{off}$ MEASUREMENT CIRCUIT



CIRCUIT FOR MEASUREMENT OF PROPAGATION DELAY



NOTES:

- (1) These ratings are limiting values above which the serviceability of any individual semiconductor device may be impaired.
- (2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- (3) These ratings give a maximum junction temperature of 200°C and junction-to-ambient thermal resistance of 14°C/watt (derating factor of 6.85 mW/°C). Junction-to-ambient thermal resistance of 486°C/watt (derating factor of 2.06 mW/°C).
- (4) Rating refers to a high-current point where collector-to-emitter voltage is lowest. For more information send for SGS Publication AR 5.
- (5) Pulse Conditions: length = 300 μsec; duty cycle = 1%.
- (6) See switching circuits for exact values of I_C , I_{B1} , and I_{B2} .