Terminal Voltage ±5V, 100 Taps

X9C102/103/104/503

E²POT™ Nonvolatile Digital Potentiometer

FEATURES

- **Compatible with X9102/103/104/503**
- **Low Power CMOS**
  - **—V_{CC} = 5V**
  - **—Active Current, 3mA Max**
  - **—Standby Current, 500μA Max**
- **99 Resistive Elements**
  - **—Temperature Compensated**
  - **—± 20% End to End Resistance Range**
- **100 Wiper Tap Points**
  - **—Wiper Positioned via Three-Wire Interface**
  - **—Similar to TTL Up/Down Counter**
  - **—Wiper Position Stored in Nonvolatile Memory and Recalled on Power-Up**
- **100 Year Wiper Position Data Retention**
- **X9C102 = 1KΩ**
- **X9C103 = 10KΩ**
- **X9C503 = 50KΩ**
- **X9C104 = 100KΩ**

DESCRIPTION

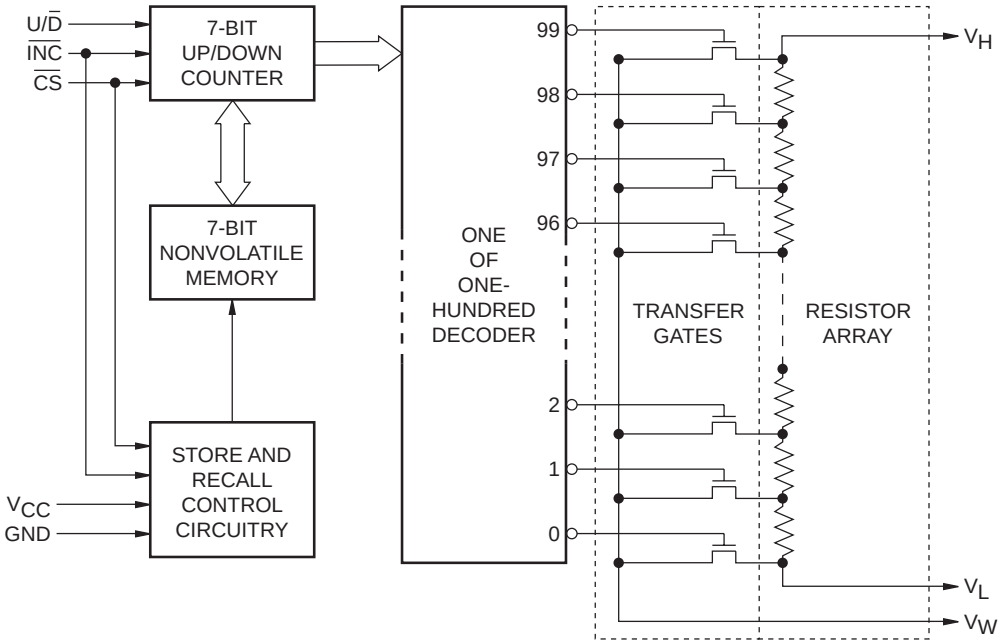
The Xicor X9C102/103/104/503 is a solid state nonvolatile potentiometer and is ideal for digitally controlled resistance trimming.

The X9C102/103/104/503 is a resistor array composed of 99 resistive elements. Between each element and at either end are tap points accessible to the wiper element. The position of the wiper element is controlled by the $\overline{CS}$, U/$\overline{D}$, and $\overline{INC}$ inputs. The position of the wiper can be stored in nonvolatile memory and then be recalled upon a subsequent power-up operation.

The resolution of the X9C102/103/104/503 is equal to the maximum resistance value divided by 99. As an example, for the X9C503 (50KΩ) each tap point represents 505Ω.

All Xicor nonvolatile memories are designed and tested for applications requiring extended endurance and data retention.

FUNCTIONAL DIAGRAM



3863 FHD F01

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PIN DESCRIPTIONS

V_H and V_L

The high (V_H) and low (V_L) terminals of the X9C102/103/104/503 are equivalent to the fixed terminals of a mechanical potentiometer. The minimum voltage is –5V and the maximum is +5V. It should be noted that the terminology of V_L and V_H references the relative position of the terminal in relation to wiper movement direction selected by the U/$\overline{D}$ input and not the voltage potential on the terminal.

V_W

V_W is the wiper terminal, equivalent to the movable terminal of a mechanical potentiometer. The position of the wiper within the array is determined by the control inputs. The wiper terminal series resistance is typically 40Ω.

Up/Down (U/$\overline{D}$)

The U/$\overline{D}$ input controls the direction of the wiper movement and whether the counter is incremented or decremented.

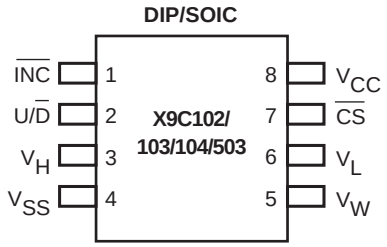
Increment ($\overline{INC}$)

The $\overline{INC}$ input is negative-edge triggered. Toggling $\overline{INC}$ will move the wiper and either increment or decrement the counter in the direction indicated by the logic level on the U/$\overline{D}$ input.

Chip Select ($\overline{CS}$)

The device is selected when the $\overline{CS}$ input is LOW. The current counter value is stored in nonvolatile memory when $\overline{CS}$ is returned HIGH while the $\overline{INC}$ input is also HIGH. After the store operation is complete the X9C102/103/104/503 will be placed in the low power standby mode until the device is selected once again.

PIN CONFIGURATION



3863 FHD F02.2

PIN NAMES

Symbol	Description
V_H	High Terminal
V_W	Wiper Terminal
V_L	Low Terminal
V_{SS}	Ground
V_{CC}	Supply Voltage
U/$\overline{D}$	Up/Down Input
$\overline{INC}$	Increment Input
$\overline{CS}$	Chip Select Input
NC	No Connect

3863 PGM T01

DEVICE OPERATION

There are three sections of the X9C102/103/104/503: the input control, counter and decode section; the nonvolatile memory; and the resistor array. The input control section operates just like an up/down counter. The output of this counter is decoded to turn on a single electronic switch connecting a point on the resistor array to the wiper output. Under the proper conditions the contents of the counter can be stored in nonvolatile memory and retained for future use. The resistor array is comprised of 99 individual resistors connected in series. At either end of the array and between each resistor is an electronic switch that transfers the potential at that point to the wiper.

The $\overline{INC}$, U/$\overline{D}$ and $\overline{CS}$ inputs control the movement of the wiper along the resistor array. With $\overline{CS}$ set LOW the X9C102/103/104/503 is selected and enabled to respond to the U/$\overline{D}$ and $\overline{INC}$ inputs. HIGH to LOW transitions on $\overline{INC}$ will increment or decrement (depending on the state of the U/$\overline{D}$ input) a seven-bit counter. The output of this counter is decoded to select one of one-hundred wiper positions along the resistive array.

The wiper, when at either fixed terminal, acts like its mechanical equivalent and does not move beyond the last position. That is, the counter does not wrap around when clocked to either extreme.

The value of the counter is stored in nonvolatile memory whenever $\overline{CS}$ transistions HIGH while the $\overline{INC}$ input is also HIGH.

When the X9C102/103/104/503 is powered-down, the last counter position stored will be maintained in the nonvolatile memory. When power is restored, the contents of the memory are recalled and the counter is reset to the value last stored.

OPERATION NOTES

The system may select the X9C102/103/104/503, move the wiper, and deselect the device without having to store the latest wiper, position in nonvolatile memory. The wiper movement is performed as described above; once the new position is reached, the system would the keep $\overline{INC}$ LOW while taking $\overline{CS}$ HIGH. The new wiper position would be maintained until changed by the system or until a power-down/up cycle recalled the previously stored data.

This would allow the system to always power-up to a preset value stored in nonvolatile memory; then during system operation minor adjustments could be made. The adjustments might be based on user preference: system parameter changes due to temperature drift, etc...

The state of U/$\overline{D}$ may be changed while $\overline{CS}$ remains LOW. This allows the host system to enable the X9C102/103/104/503 and then move the wiper up and down until the proper trim is attained.

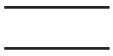
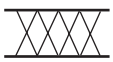
T_{IW}/R_{TOTAL}

The electronic switches on the X9C102/103/104/503 operate in a “make before break” mode when the wiper changes tap positions. If the wiper is moved several positions, multiple taps are connected to the wiper for t_{IW} ($\overline{INC}$ to V_W change). The R_{TOTAL} value for the device can temporarily be reduced by a significant amount if the wiper is moved several positions.

R_{TOTAL} with V_{CC} Removed

The end to end resistance of the array will fluctuate once V_{CC} is removed.

SYMBOL TABLE

WAVEFORM	INPUTS	OUTPUTS
	Must be steady	Will be steady
	May change from LOW to HIGH	Will change from LOW to HIGH
	May change from HIGH to LOW	Will change from HIGH to LOW
	Don’t Care: Changes Allowed	Changing: State Not Known
	N/A	Center Line is High Impedance

ABSOLUTE MAXIMUM RATINGS*

Temperature under Bias ..... –65°C to +135°C
Storage Temperature ..... –65°C to +150°C
Voltage on $\overline{CS}$, $\overline{INC}$, U/$\overline{D}$ and V_{CC}
with Respect to V_{SS} ..... –1V to +7V
Voltage on V_H and V_L
Referenced to V_{SS} ..... –8V to +8V
$\Delta V = |V_H - V_L|$
X9C102 ..... 4V
X9C103, X9C503, and X9C104 ..... 10V
Lead Temperature (Soldering, 10 seconds) .... +300°C
Wiper Current ..... ±1mA

*COMMENT

Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and the functional operation of the device at these or any other conditions above those listed in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ANALOG CHARACTERISTICS

Electrical Characteristics

End-to-End Resistance Tolerance ..... ±20%
Power Rating at 25°C
X9C102 ..... 16mW
X9C103, X9C503, and X9C104 ..... 10mW
Wiper Current ..... ±1mA Max.
Typical Wiper Resistance ..... 40Ω at 1mA
Typical Noise ..... < –120dB/$\sqrt{}$ Hz Ref: 1V

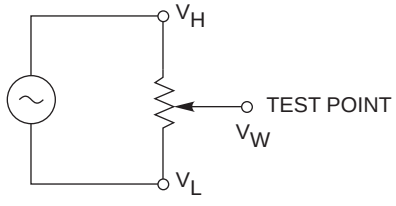
Resolution

Resistance 1%

Linearity

Absolute Linearity(1) ..... ±1.0 Ml(2)
Relative Linearity(3) ..... ±0.2 Ml(2)

Test Circuit #1



3863 FHD F04

Temperature Coefficient

(–40°C to +85°C)
X9C102 ..... +600 ppm/°C Typical
X9C103, X9C503, X9C104 ..... +300 ppm/°C Typical
Ratiometric Temperature Coefficient ..... ±20 ppm

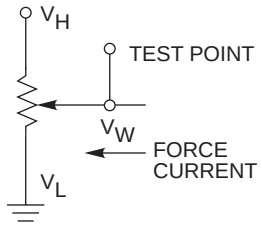
Wiper Adjustability

Unlimited Wiper Adjustment (Non-Store operation)
Wiper Position Store Operations ..... 10,000 Data Changes

Physical Characteristics

Marking Includes
Manufacturer‘s Trademark
Resistance Value or Code
Date Code

Test Circuit #2



3863 FHD F05

Notes: (1) Absolute Linearity is utilized to determine actual wiper voltage versus expected voltage = ($V_{w(n)}$(actual) – $V_{w(n)}$(expected)) = ±1 Ml Maximum.
(2) 1 Ml = Minimum Increment = R_{TOT}/99.
(3) Relative Linearity is a measure of the error in step size between taps = $V_{W(n+1)}$ – [$V_{w(n)}$ + Ml] = +0.2 Ml.

RECOMMENDED OPERATING CONDITIONS

Temperature	Min.	Max.
Commercial	0°C	+70°C
Industrial	–40°C	+85°C
Military	–55°C	+125°C

3863 PGM T03.1

Supply Voltage	Limits
X9C102/103/104/503	5V ±10%

3863 PGM T04.2

D.C. OPERATING CHARACTERISTICS (Over recommended operating conditions unless otherwise specified.)

Symbol	Parameter	Limits			Units	Test Conditions
		Min.	Typ.(4)	Max.		
I_{CC}	V_{CC} Active Current		1	3	mA	$\overline{CS}$ = V_{IL}, U/$\overline{D}$ = V_{IL} or V_{IH} and $\overline{INC}$ = 0.4V to 2.4V @ max. t_{CYC}
I_{SB}	Standby Supply Current		200	500	µA	$\overline{CS}$ = V_{CC} – 0.3V, U/$\overline{D}$ and $\overline{INC}$ = V_{SS} or V_{CC} – 0.3V
I_{LI}	$\overline{CS}$, $\overline{INC}$, U/$\overline{D}$ Input Leakage Current			±10	µA	V_{IN} = V_{SS} to V_{CC}
V_{IH}	$\overline{CS}$, $\overline{INC}$, U/$\overline{D}$ Input HIGH Voltage	2		V_{CC} + 1	V	
V_{IL}	$\overline{CS}$, $\overline{INC}$, U/$\overline{D}$ Input LOW Voltage	–1		0.8	V	
R_W	Wiper Resistence		40	100	Ω	Max. Wiper Current ±1mA
V_H	VH Terminal Voltage	–5		+5	V	
V_L	VL Terminal Voltage	–5		+5	V	
C_{IN}(5)	$\overline{CS}$, $\overline{INC}$, U/$\overline{D}$ Input Capacitance			10	pF	V_{CC} = 5V, V_{IN} = V_{SS}, T_A = 25°C, f = 1MHz

3863 PGM T05.3

STANDARD PARTS

Part Number	Maximum Resistance	Wiper Increments	Minimum Resistance
X9C102	1KΩ	10.1Ω	40Ω
X9C103	10KΩ	101Ω	40Ω
X9C503	50KΩ	505Ω	40Ω
X9C104	100KΩ	1010Ω	40Ω

3863 PGM T08.1

Notes: (4) Typical values are for T_A = 25°C and nominal supply voltage.
(5) This parameter is periodically sampled and not 100% tested.

A.C. CONDITIONS OF TEST

Input Pulse Levels	0V to 3V
Input Rise and Fall Times	10ns
Input Reference Levels	1.5V

3863 PGM T05.1

MODE SELECTION

$\overline{CS}$	$\overline{INC}$	U/$\overline{D}$	Mode
L	↘	H	Wiper Up
L	↘	L	Wiper Down
↗	H	X	Store Wiper Position
H	X	X	Standby Current
↗	L	X	No Store, Return to Standby

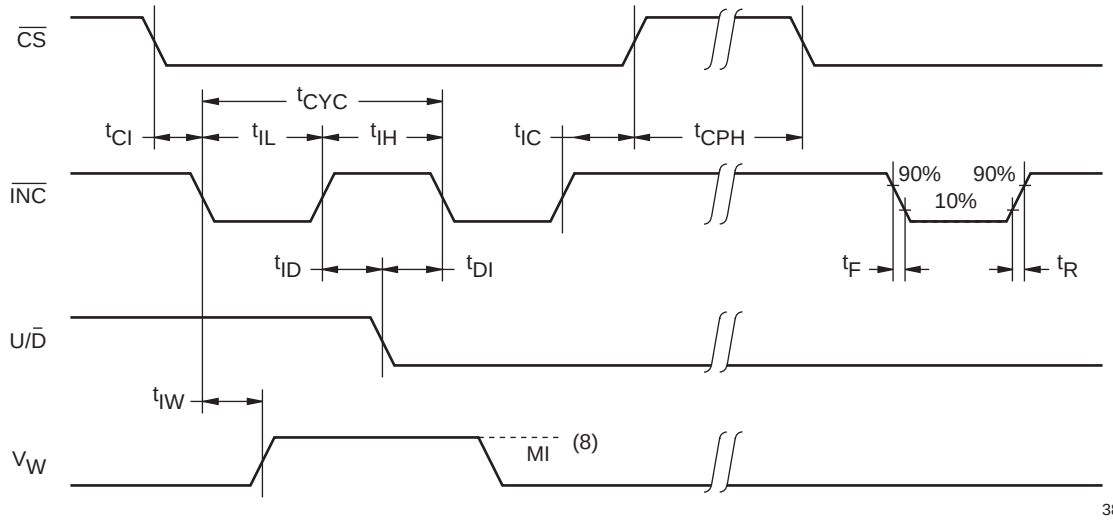
3863 PGM T06

A.C. OPERATING CHARACTERISTICS (Over recommended operating conditions unless otherwise specified)

Symbol	Parameter	Limits			Units
		Min.	Typ.(6)	Max.	
t_{CI}	$\overline{CS}$ to $\overline{INC}$ Setup	100			ns
t_{ID}	$\overline{INC}$ HIGH to U/$\overline{D}$ Change	100			ns
t_{DI}	U/$\overline{D}$ to $\overline{INC}$ Setup	2.9			µs
t_{IL}	$\overline{INC}$ LOW Period	1			µs
t_{IH}	$\overline{INC}$ HIGH Period	1			µs
t_{IC}	$\overline{INC}$ Inactive to $\overline{CS}$ Inactive	1			µs
t_{CPH}	$\overline{CS}$ Deselect Time	20			ms
t_{IW}	$\overline{INC}$ to Vw Change		100	500	µs
t_{CYC}	$\overline{INC}$ Cycle Time	4			µs
t_R, t_F(7)	$\overline{INC}$ Input Rise and Fall Time			500	µs
t_{PU}(7)	Power up to Wiper Stable			500	µs
t_R V_{CC}(7)	V_{CC} Power-up Rate	0.2		50	mV/µs

3863 PGM T07.3

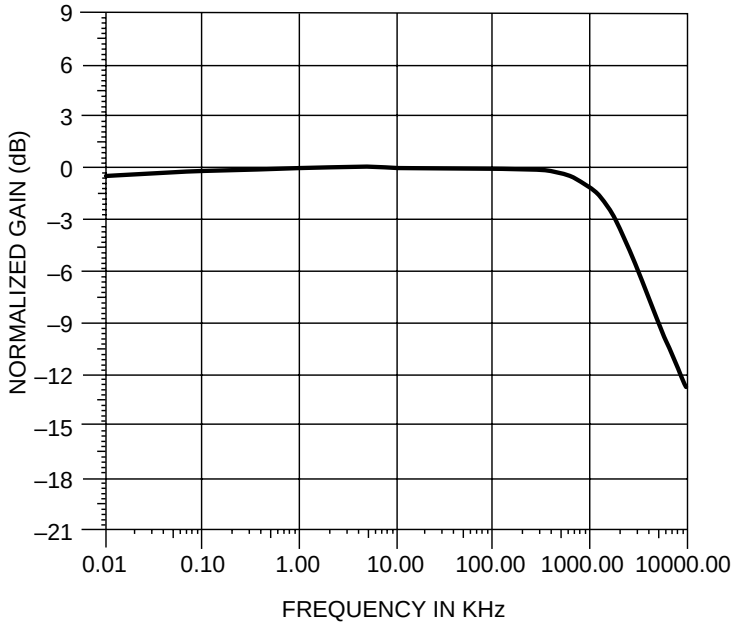
A.C. Timing



3863 FHD F03

Notes: (6) Typical values are for T_A = 25°C and nominal supply voltage.
(7) This parameter is periodically sampled and not 100% tested.
(8) MI in the A.C. timing diagram refers to the minimum incremental change in the V_W output due to a change in the wiper position.

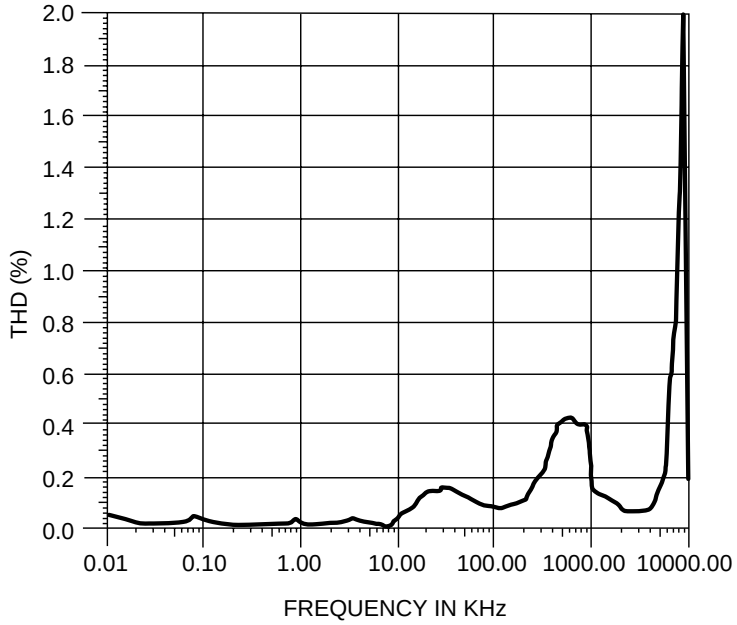
Typical Frequency Response for X9C102



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 0.5V_{RMS}$
Normalized (0dB @ 1KHz)
Test Circuit #1

3863 FHD F06

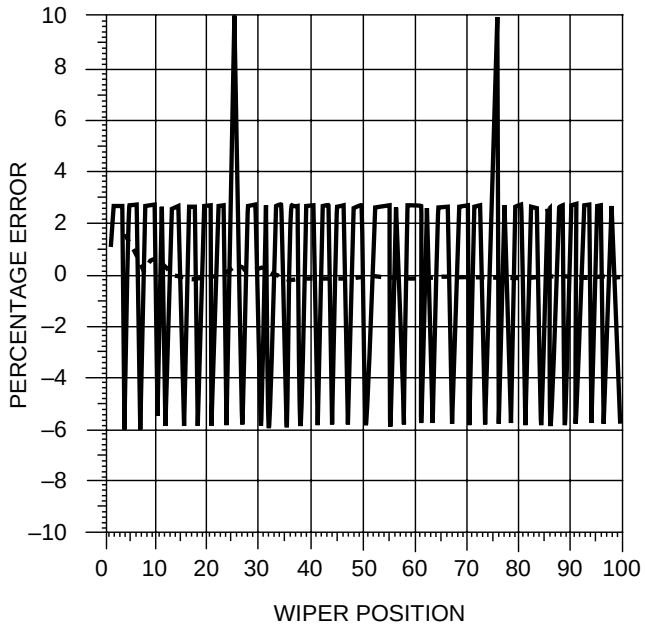
Typical Total Harmonic Distortion for X9C102



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 2V_{RMS}$
Test Circuit #1

3863 FHD F07

Typical Linearity for X9C102

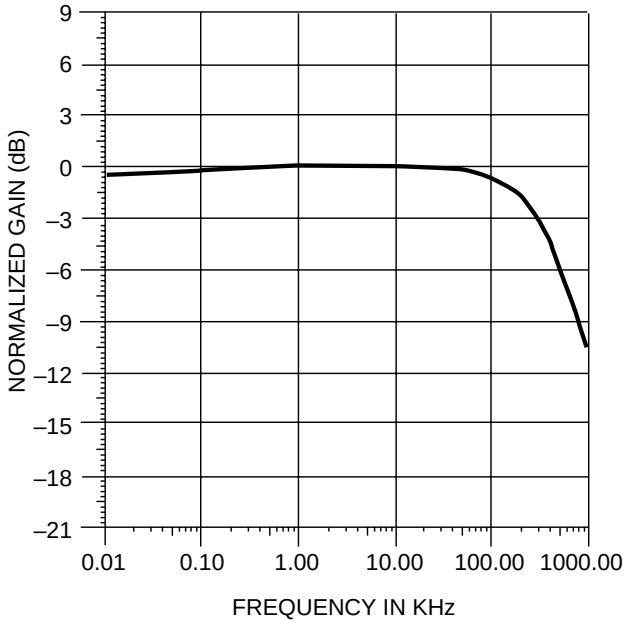


TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Test Circuit #2

KEY:
= ABSOLUTE
= RELATIVE

3863 FHD F08

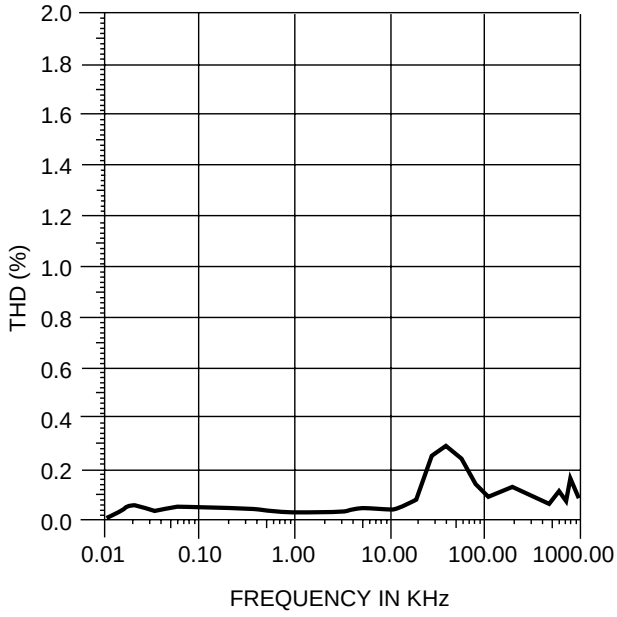
Typical Frequency Response for X9C103



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 0.5V_{RMS}$
Normalized (0dB @ 1KHz)
Test Circuit #1

3863 FHD F09

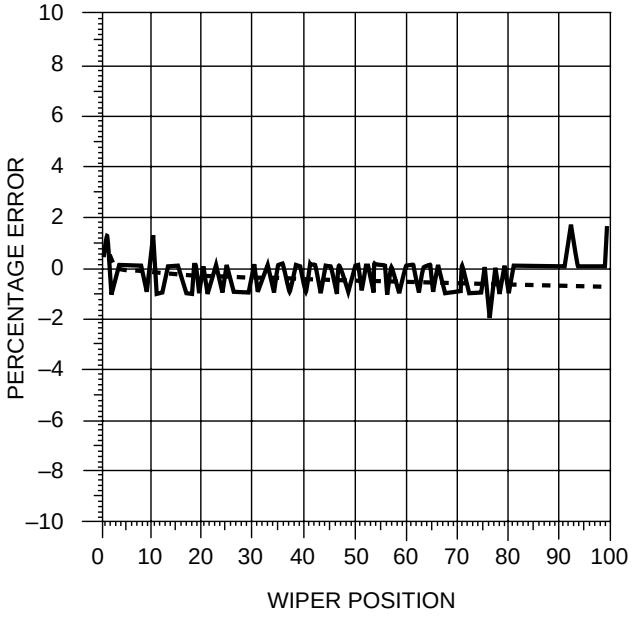
Typical Total Harmonic Distortion for X9C103



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 2V_{RMS}$
Test Circuit #1

3863 FHD F10

Typical Linearity for X9C103

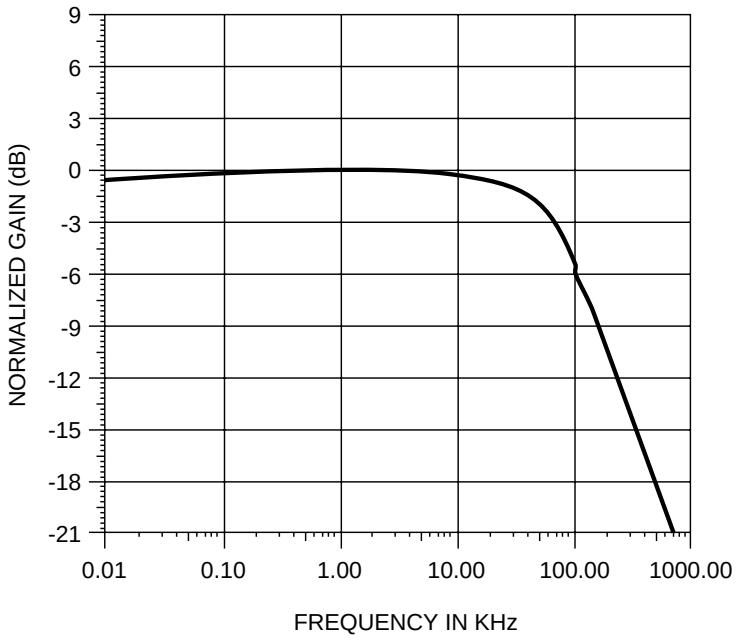


TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Test Circuit #2

KEY:
= ABSOLUTE
= RELATIVE

3863 FHD F11

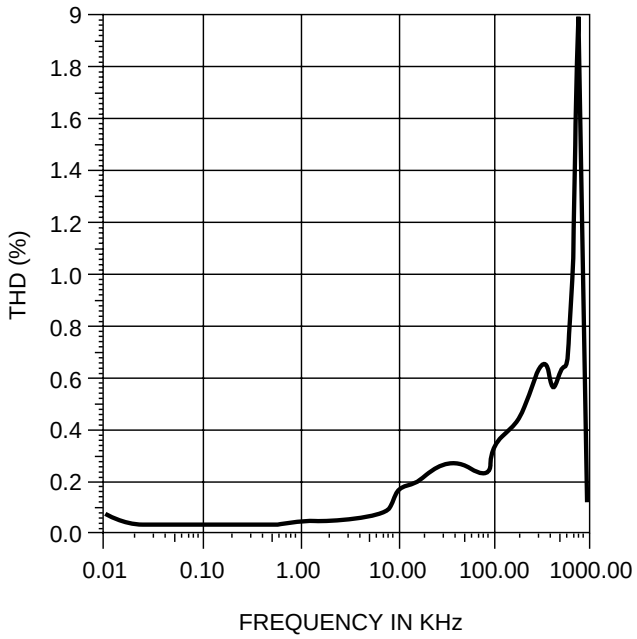
Typical Frequency Response for X9C503



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 0.5V_{RMS}$
Normalized (0dB @ 1 KHz)
Test Circuit #1

3863 FHD F12

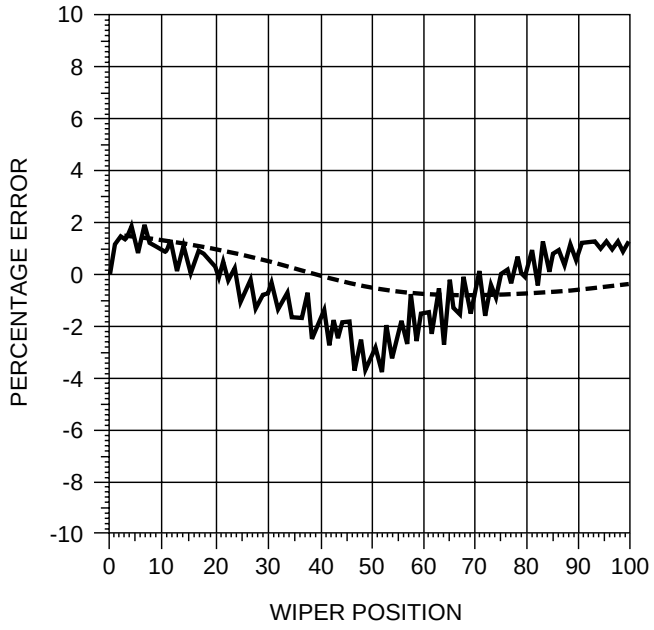
Typical Total Harmonic Distortion for X9C503



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 2V_{RMS}$
Test Circuit #1

3863 FHD F13

Typical Linearity for X9C503

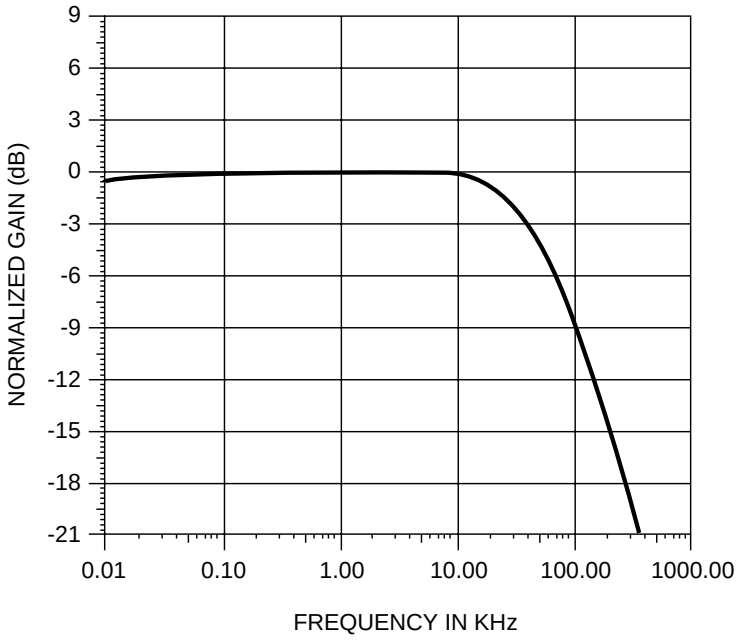


TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Test Circuit #2

KEY:
- - - - - - - = ABSOLUTE
——— = RELATIVE

3863 FHD F14

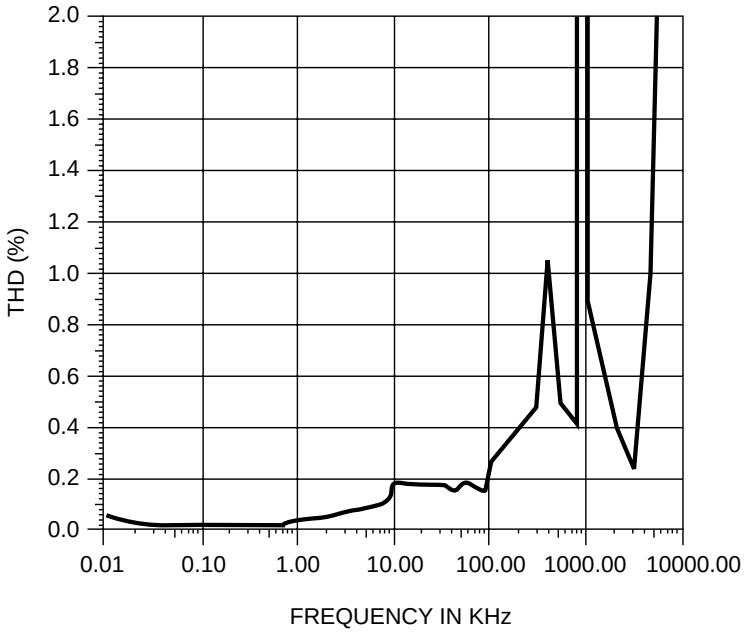
Typical Frequency Response for X9C104



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 0.5V_{RMS}$
Normalized (0dB @ 1 KHz)
Test Circuit #1

3863 FHD F15

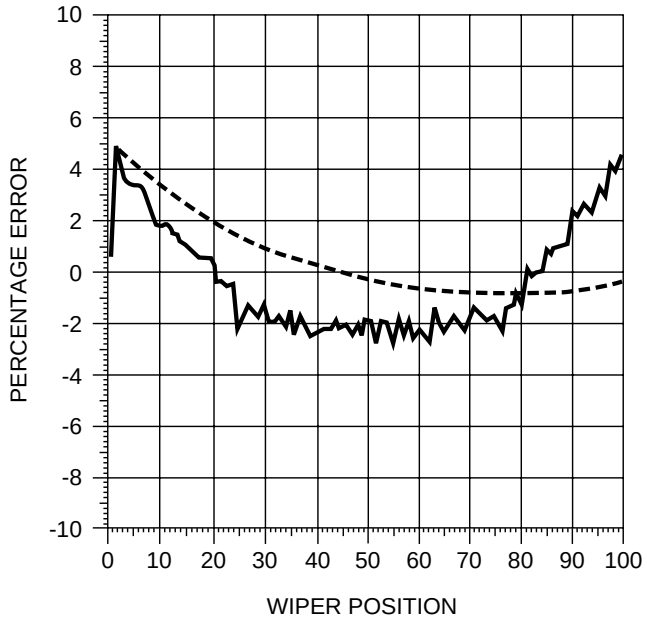
Typical Total Harmonic Distortion for X9C104



TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Wiper @ Tap 50
$V_H = 2V_{RMS}$
Test Circuit #1

3863 FHD F16

Typical Linearity for X9C104



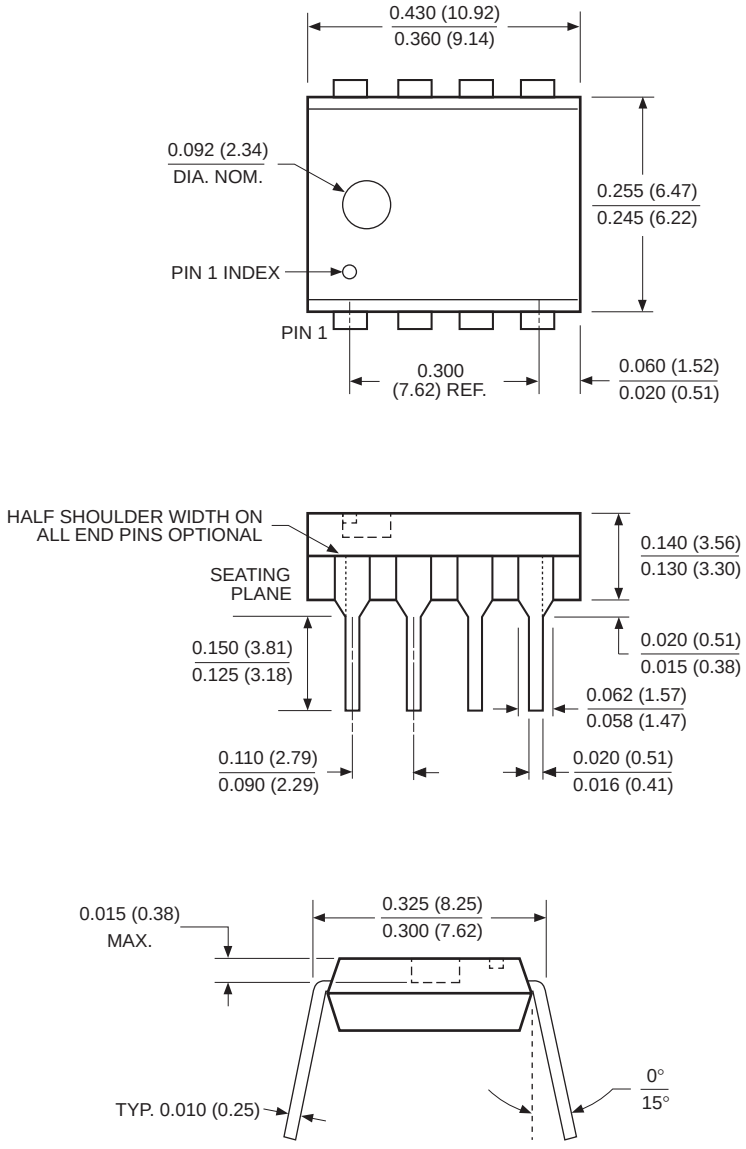
TEST CONDITIONS
V_{CC} = 5V
Temp. = 25°C
Test Circuit #2

KEY:
= ABSOLUTE
= RELATIVE

3863 FHD F17

PACKAGING INFORMATION

8-LEAD PLASTIC DUAL IN-LINE PACKAGE TYPE P

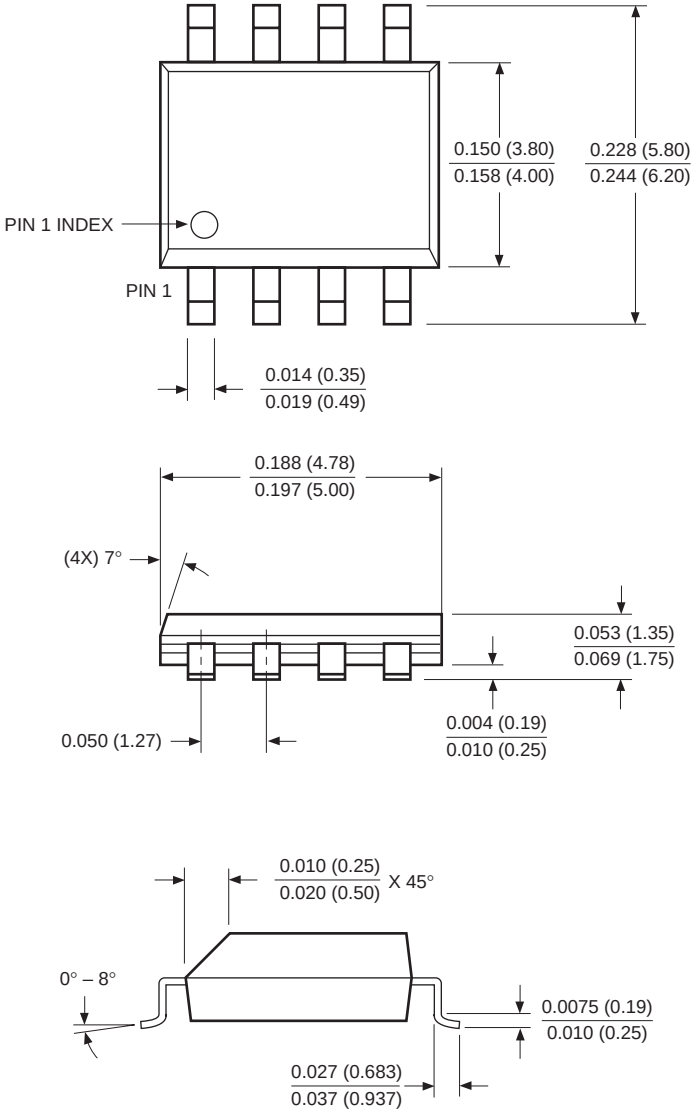


NOTE: ALL DIMENSIONS IN INCHES (IN PARENTHESES IN MILLIMETERS)

3926 FHD F01

PACKAGING INFORMATION

8-LEAD PLASTIC SMALL OUTLINE GULL WING PACKAGE TYPE S



NOTE: ALL DIMENSIONS IN INCHES (IN PARENTHESIS IN MILLIMETERS)

3926 FHD F22

ORDERING INFORMATION