Audio ICs

High-definition sound processor BA3880S

The BA3880S is a sound processor IC that performs phase and harmonic compensation on audio signals to accurately reproduce the "rise" section of audio signals that determines the characteristics of the sound, and thus reproduce the original recording as naturally as possible.

Applications

Component stereo systems, radio cassette players, car stereo systems, televisions, VCRs, and active speakers.

Features

- Corrects distortion in the rising section of audio signals that results from miss-matched speakers and amplifiers, and reproduces the original sound faithfully.
- To allow application with all types of speakers and sources, the clarity can be adjusted using DC control.
- DC control provided for selection of either processor mode or bypass mode.
- 4) Bass boost fixed internally to 4dB.

• Absolute maximum ratings (Ta = 25° C)

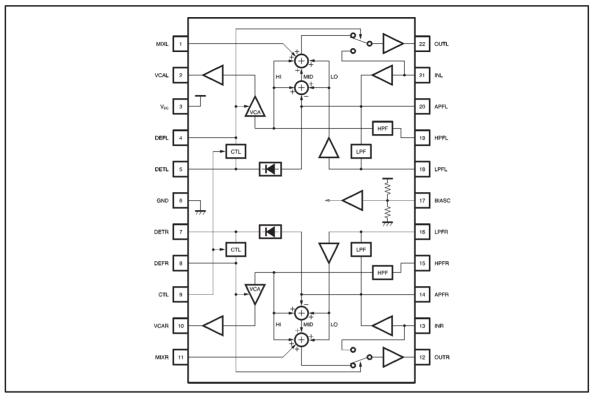
Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	11	V
Power dissipation	Pd	1000 *	mW
Operating temperature	Topr	-40~+85	Ĉ
Storage temperature	Tstg	-55~+125	Ĵ

* Reduced by 10mW for each increase in Ta of 1°C over 25°C.

Recommended operating conditions

Parameter	Symbol	Limits	Unit
Power supply voltage	V _{cc}	5.4~10.8	V

Block diagram





●Electrical characteristics(unless otherwise noted, Ta = 25°C, Vcc = 9V, fiN = 1kHz, ViN = 200mVrms,
$R_0 = 600\Omega$, $R_L = 100k\Omega$ and $V_{CTL} = 4.5V$)

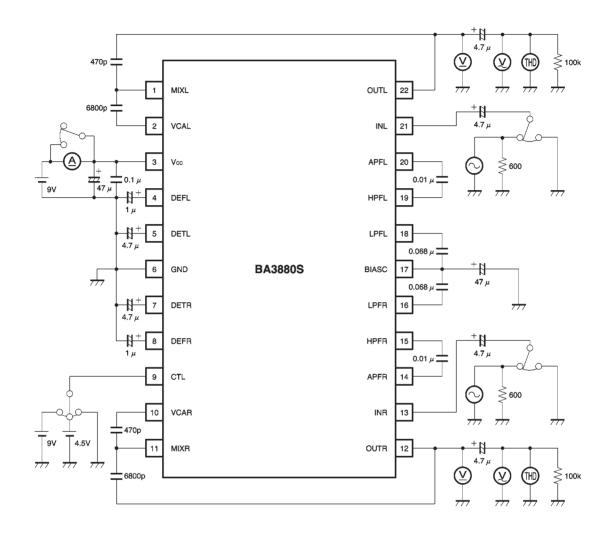
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Circuit current	lcc	4.2	6.2	9.3	mA	VIN=0Vms
Voltage gain 1	Gv1	-2.0	0	2.0	dB	V _{CTL} =9V
Voltage gain 2	Gv2	-2.5	-0.5	1.5	dB	
Voltage gain 3	Gv3	-1.0	1.0	3.0	dB	fin=10kHz
Voltage gain 4	Gv4	9.0	11.0	13.0	dB	fin=10kHz, Vctl=0V
Voltage gain 5	Gv5	-	4.0	-	dB	fin=50Hz
Channel balance	СВ	-2.0	0	2.0	dB	fin=10kHz, Vct⊾=0V
Maximum output voltage	Vом	2.0	2.3	-	Vrms	THD=1%
Output noise voltage 1	VNO1	-	2	10	μ Vrms	$R_g=0\Omega$, DIN AUDIO, VCTL=9V
Output noise voltage 2	VNO2	-	16	70	μ Vrms	$R_g=0\Omega$, DIN AUDIO
Total harmonic distortion 1	THD1	-	0.005	0.05	%	400Hz~30kHz BPF, VctL=9V
Total harmonic distortion 2	THD ₂	-	0.04	0.4	%	400Hz~30kHz BPF
Channel separation	CS	-	-67	-60	dB	VIN=1Vrms
Ripple rejection ratio	RR	60	68	-	dB	$R_g=0\Omega$, f _R =100Hz, V _R =100mV _{ms}

*Note : When $V_{CTL} = 9V$, the IC is in bypass mode (through operations).

When $V_{CTL} = 4.5V$, the IC is in processor mode (enhance operations) with minimum definition level.

When VcrL = 0V, the IC is in processor mode (enhance operations) with maximum definition level.

Measurement circuit



(Units) R:Ω C:F

Fig. 1

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Circuit operation

The BA3880S high-definition sound processor system IC treats the amplifier and speakers as a total audio system, and performs signal processing at the amplifier input stage to accurately reproduce the rise sections of audio signals that determine the characteristics of sound, and ensure that the replayed audio is as close to the original sound and as natural sounding as possible.

Problems relating to playback of sound on audio systems.

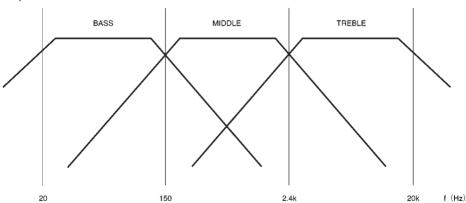
With naturally-generated sound, high-frequency harmonic components generally occur first, followed by the fundamental frequency components. This also applies to the attack section of the sound that characterizes many musical instruments. It is the amplitude component that gives form to the frequency component and envelope at the rising point of sound, and this expresses the characteristics of the sound. Therefore, when replaying audio on audio playback systems, it is extremely important that the rise section of the signal be expressed accurately with respect to the original sound.

However, there is inevitable mismatching between the speakers and amplifiers in current audio systems. Solidstate power amplifiers use negative feedback techniques and operate off a fixed voltage supply, but as speakers are a current element, mismatching in the system inevitably occurs. In addition, the speaker impedance characteristics are strongly influenced by the electrical reactance of the voice coil, and the mechanical reactance of the cone assembly. As a result of this, distortion is generated in the rising sections of audio signals, causing phase shift in the reproduced sound. In addition, impedance increases in the treble region which reduces the speaker amplitude and reduces the harmonic component, and as a result there is a tendency to masking of large mid-frequency signal components that follow, and this makes it difficult to reproduce the rising sections of the sound.

Operating principle

To solve the problems described above, the BA3880S performs the audio signal processing described below.

Phase shift compensation



The input signal is separated into three regions: bass (20 Hz to 150 Hz), middle (150 Hz to 2.4kHz) and treble (2.4kHz to 20kHz). These regions are then joined again with the middle and treble regions phase shifted -180° and -360° respectively with respect to the bass signal. This phase shift adjusts the delay time characteristic for each band and minimizes the distortion of the rising section of the audio signal.

Amplitude compensation (harmonic compensation) To maintain appropriate auditory balance for the treble region that is made up of harmonic components, a highspeed detector and high-performance VCA circuit are used for amplitude control of the treble component. The amount of compensation is determined from a calculation performed based on the DC level input to the CTL pin, and the DC level detected from the input signal. The internal control signal obtained from this calculation compensates the amplitude of the treble component input to the VCA. This amplitude compensation recovers the harmonic component, and improves the reproduction clarity (definition level) of instruments and vocals.

Bass boost

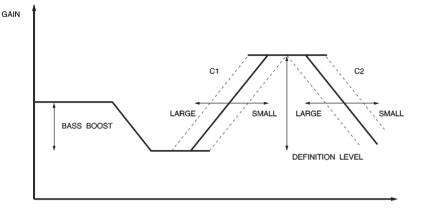
To maintain good balance with the dynamically changing treble component, the bass component is boosted. The bass boost is fixed at 4dB (typ.) internally, and cannot be adjusted.

Mode switch

The DC voltage on the CTL pin can be used to switch the IC between processing mode and bypass mode. When processing mode is selected, the audio signal is enhanced using the compensation circuits described above before being output. When bypass mode is selected, the signal bypasses all of the compensation circuits, and only passes through a buffer amplifier before output.

External components

The frequency characteristic in processing mode with maximum definition level is given below.



- C1: External capacitor connected between the VCA and MIX pins (6800pF in the application circuit).
- C2: External capacitor connected between the OUT and MIX pins (470pF in the application circuit).

FREQUENCY

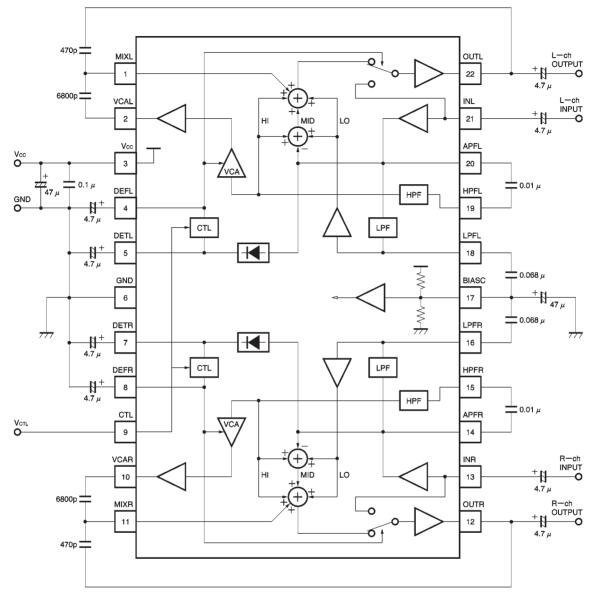
As indicated in the diagram, capacitors C1 and C2 determine the treble component cutoff frequency. By varying the values of these components, the frequency characteristic can be changed as indicated by the dotted line in the diagram. This varies the balance of the harmonic component, and is an important factor in determining the tone quality.

Control table

Item	Threshold level	Mode setting	Definition level
V _{CTL}	— Vcc	Recommended setting range for bypass mode	
	0.6×Vcc		Min. (1dB Typ.)
	-	0.5×Vcc Recommended setting range for processor mode	A
	– 0V (GND)		, Max. (11dB Typ.)



Application example



(Units) R:Ω C:F

Fig. 2

ROHM

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Operation notes

(1) Control pin (CTL)

The level of the DC voltage applied to the control pin should be in the range 0V to Vcc. Appropriate switching may not be possible in the region of the threshold voltage. Allow sufficient margin when setting this voltage.

(2) Recommended operating range

Provided the IC is operated within the operating temperature range and within the recommended operating conditions, the general circuit functions are guaranteed to operate correctly, and there will not be significant changes in the electrical characteristics.

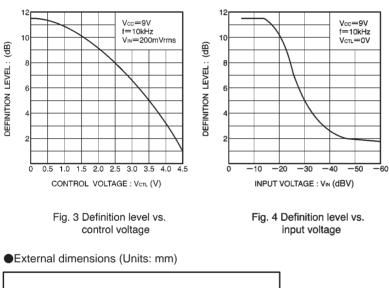
(3) Application example

Provided the circuit constants in the application example

are used, the application circuit should function correctly. However, we recommend that you confirm the characteristics of the circuit in actual use, and take note of the cautionary items described above. If you change the circuit constants, check both the static and transient characteristics of the circuit, and allow sufficient margin to accommodate variations in both the IC and the external components.

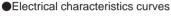
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7.62

0.3±0.1



19.4±0.3

1 778

0.51Min.

4土0.2 3.95土0.3

6.5±0.

0.5±0.1

SDIP22



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