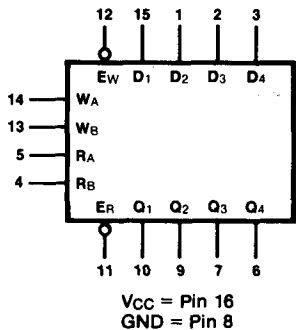
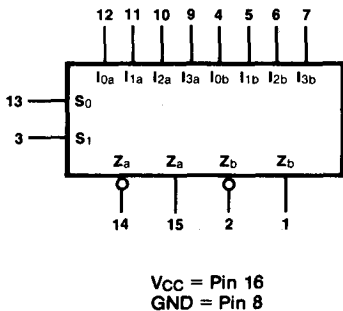


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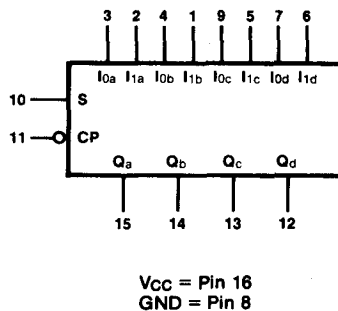
**D154**  
54/74170, 54LS/74LS170,  
54LS/74LS670



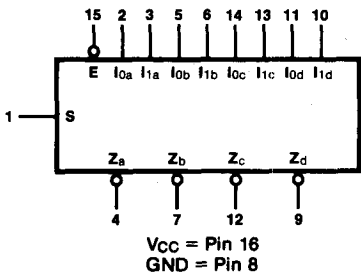
**D155**  
9309, 93L09



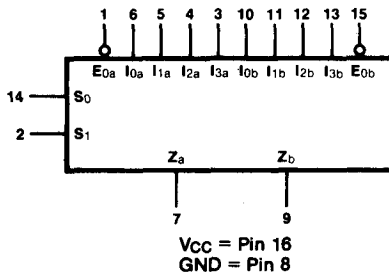
**D156**  
54/74298, 54LS/74LS298



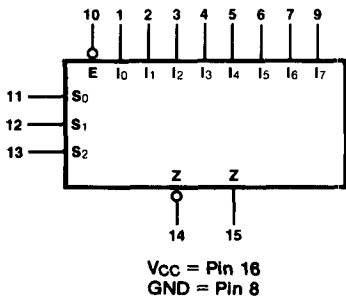
**D157**  
9322, 93L22, 54/74157,  
54S/74S157, 54LS/74LS157, 54S/74S158,  
54LS/74LS158, 54S/74S257, 54LS/74LS257,  
54S/74S258, 54LS/74LS258



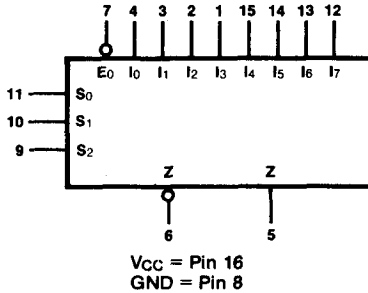
**D158**  
54/74153, 54S/74S153, 54LS/74LS153,  
54S/74S253, 54LS/74LS253



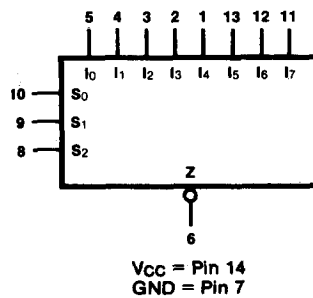
**D159**  
9312, 93L12, 93S12, 9313



**D160**  
54/74151A, 54S/74S151,  
54LS/74LS151, 54S/74S251,  
54LS/74LS251



**D161**  
54/74152A, 54LS/74LS152



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## TTL

### MULTIPLEXERS (Cont'd)

Item	Function	DEVICE NO.	Enable Inputs	True Output <sup>(1)</sup>	Complement Output <sup>(1)</sup>	Select Delay ns (Typ)	Enable Delay ns (Typ)	Data Delay ns (Typ)	Power Dissipation mW (Typ)	Fan-Out (UL) <sup>(2)</sup>	Logic/Connection Diagram	Packages(s)
1	8-Input	54/74151A	1	X	X	25	21	16	145	10	D160	4L,7B,9B
2	8-Input	54LS/74LS151	1	X	X	28	25	18	30	5.0	D160	4L,6B,9B
3	8-Input	54S/74S151	1	X	X	12	11	8.0	225	12.5	D160	4L,6B,9B
4	8-Input	54LS/74LS251	1	3S	3S	29	21	18	33	5.0	D160	4L,6B,9B
5	8-Input	54S/74S251	1	3S	3S	12	12	8.0	275	12.5	D160	4L,6B,9B
6	8-Input	74152A	1	—	X	18	—	8.0	130	10	D161	7A,9A
7	8-Input	54LS/74LS152	—	—	X	22	—	11	28	5.0	D161	4L,6B,9B
8	16-Input	54/74150	1	—	X	22	21	13	200	10	D162	4M,6N,9N

### DECODERS/DEMULTIPLEXERS

Item	Function	DEVICE NO.	Address Inputs	Active LOW Enable	Active LOW Outputs	Open Collector Output Voltage V	Address Delay ns (Typ)	Enable Delay ns (Typ)	Power Dissipation mW (Typ)	Fan-Out (UL) <sup>(2)</sup>	Logic/Connection Diagram	Package(s)
9	Dual 1-of-4	9321	2+2	1+1	4+4	—	14	12	150	10	D131	4L,6B,9B
10	Dual 1-of-4	93L21	2+2	1+1	4+4	—	43	34	45	5.0	D131	4L,6B,9B
11	Dual 1-of-4	54LS/74LS139	2+2	1+1	4+4	—	22	19	34	5.0	D131	4L,6B,9B
12	Dual 1-of-4	54S/74S139	2+2	1+1	4+4	—	7.5	6.0	300	12.5	D131	4L,6B,9B
13	Dual 1-of-4	54/74155	2	2+2	4+4	—	21	18	125	10	D132	4L,6B,9B
14	Dual 1-of-4	54LS/74LS155	2	2+2	4+4	—	18	15	30	5.0	D132	4L,6B,9B
15	Dual 1-of-4	54/74156	2	2+2	4+4	5.5	23	20	125	10	D132	4L,6B,9B
16	Dual 1-of-4	54LS/74LS156	2	2+2	4+4	5.5	33	26	31	5.0	D132	4L,6B,9B
17	1-of-8	9301	3	1	8	—	22	22	145	10	D133	4L,6B,9B
18	1-of-8	93L01	3	1	8	—	36	36	45	5.0	D133	4L,6B,9B
19	1-of-8	9302	3	1	8	5.5	30	30	145	10	D133	4L,6B,9B
20	1-of-8	9334	3	1	8	—	30	19	280	6.0	D134	4L,7B,9B

1. OC = open collector, 3S = 3-state
2. Unit Load (UL) = 40 $\mu$ A HIGH/1.6mA LOW