TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA7358APG

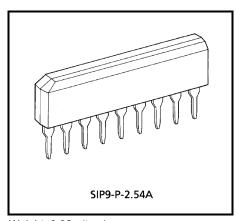
FM Front-End

The TA7358APG is designed for a FM front—end application,which is suitable to a portable radio or a radio cassette.

Comparing with conventional types, supply voltage dependence, overload characteristics and spurious radiation characteristics are improved.

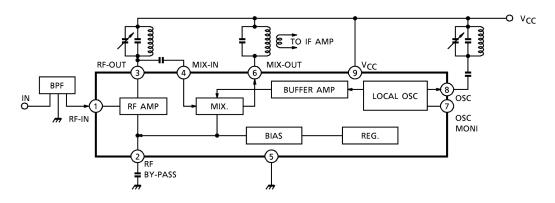
Features

- Wide supply voltage range : $V_{CC} = 1.6 \sim 6.0 \text{V}$
- Excellent supply voltage dependence of local oscillator
 - : Oscillation stop VCC = 0.9V (typ.)
- Improved inter-modulation characteristics by double balanced type mixer circuit.
- · Low spurious radiation.
- Built-in clampping diode for the local oscillator output.



Weight: 0.92g (typ.)

Block Diagram



Explanation Of Terminals (terminal voltage is DC voltage at Ta = 25°C, V_{CC} = 5V, and no signal)

Pin No.	Symbol	Internal	Terminal Voltage (V)	
1	FM-RF IN	3	0.8	
2	BY PASS	1 BIAS	1.5	
3	FM-RF OUT	GND (5)	5.0	
4	MIX IN	6 3 VCC 4 GND 5	1.5	
5	GND	_	0	
6	MIX OUT	cf. pin(4)	5.0	
7	OSC MONITOR	Vcc 9	4.3	
8	OSC	7 GND (5	5.0	
9	Vcc	_	5.0	



Maximum Ratings (Ta = 25°C)

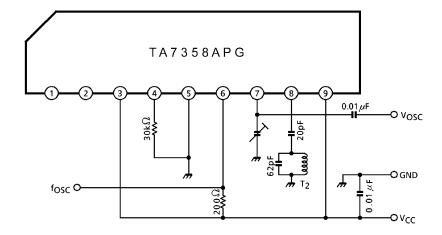
Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	8	V
Power dissipation	P _D (Note)	500	mW
Operating temperature	T _{opr}	-25~75	°C
Storage temperature	T _{stg}	-55~150	°C

(Note) Derated above 25°C in the proportion of 4mW / °C.

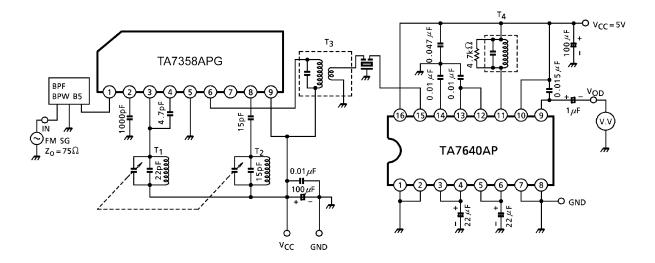
Electrical Characteristics (V_{CC} = 3V, f = 83MHz, f_m = 1kHz, Δ f = ±22.5kHz, Ta = 25°C)

Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Supply curren	t	Icc	2	V _{in} = 0	_	5.2	8.0	mA
-3dB limiting sensitivity		V _{in(lim)}	2	_	-	3.0	7.0	dBµV EMF
Quiescent sensitivity		QS	2	_	_	11.0	_	dBµV EMF
Conversion gain		G _C	_	_	_	31	_	dB
Local OSC vo	Local OSC voltage		1	f _{OSC} = 60MHz	90	165	220	mV_{rms}
Pin (1) impedance	Parallel input resistance	r _{ip} 1	3		_	57	_	Ω
Pin (3) impedance	Parallel output resistance	r _{op} 3	3		_	25	_	kΩ
	Parallel output capacitance	c _{op} 3	3		_	2.0	_	pF
Pin (4) impedance	Parallel input resistance	r _{ip} 4	2		_	2.7	_	kΩ
	Parallel input capacitance	c _{ip} 4	3		_	3.3	_	pF
Pin (6) impedance	Parallel output resistance	r _{op} 6		6 - 40 7MU-	_	100	_	kΩ
	Parallel output capacitance	c _{op} 6	3	f = 10.7MHz	_	4.8	_	pF
Local OSC stop voltage		V _{stop}	1	_	_	0.9	1.3	V

Test Circuit 1



Test Circuit 2

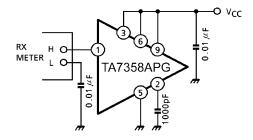


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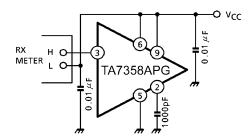
Test Circuit 3

Input output impedance

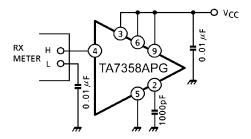
(1) r_{ip1}, c_{ip1}



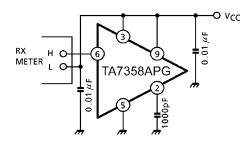
(2) r_{op3}, c_{op3}



(3) r_{ip4}, c_{ip4}



(4) r_{op6}, c_{op6}



Test Circuit Coil Data (Japan band for 76.0MHz to 108.0MHz)

Coil	f _O	Q _o	Turns	Capacitance	
T ₁ RF coil	100MHz	100	0.5 mm ϕ $2\frac{1}{4}$ T Center tap (Japan band)	15pF (external)	FERRITE CORE
T ₂ OSC coil	100MHz	100	0.5 mm ϕ $2\frac{1}{2}$ T (Japan band)	15pF (external)	FERRITE CORE
T ₃ IFT coil	10.7MHz	115	(1)–(3) 12T (4)–(6) 1T Wire 0.12mm $_{\phi}$ UEW SUMIDA ELECTRIC Co., LTD 5764 or equivalent	75pF	Pin (BOTTOM VIEW)
T ₄ Quad coil	10.7MHz	150	(4)–(6) 14T Wire 0.12mm SUMIDA ELECTRIC Co., LTD 44M–933A or equivalent	47pF	(BOTTOM VIEW)

Band pass filter (BPF)

SOSHIN ELECTRIC Co., LTD. BPWB5

Tuning cpacitor

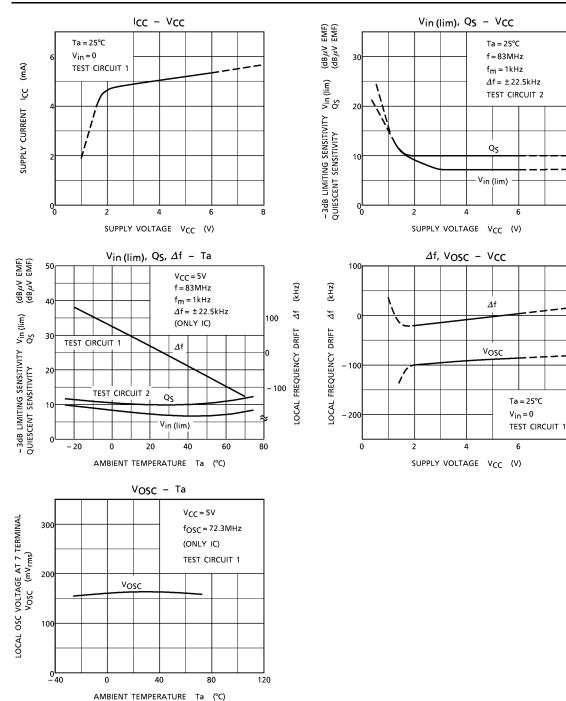
ALPS ELECTRIC Co., LTD. CB41EL933

LOCAL OSC VOLTAGE AT 7 TERMINAL VOSC (mVrms)

300

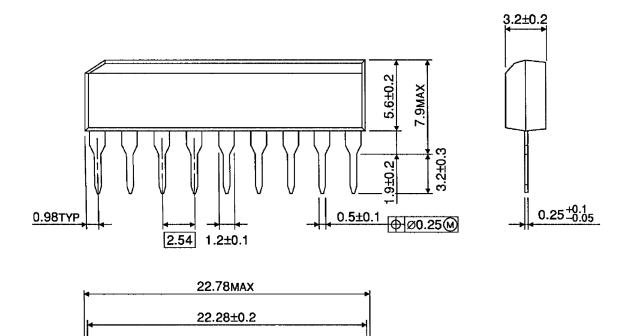
200

100



Package Dimensions

SIP9-P-2.54A Unit: mm



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Weight: 0.92g (typ.)

About solderability, following conditions were confirmed

- Solderability
 - (1) Use of Sn-63Pb solder Bath
 - solder bath temperature = 230°C
 - · dipping time = 5 seconds
 - · the number of times = once
 - · use of R-type flux
 - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - · solder bath temperature = 245°C
 - dipping time = 5 seconds
 - · the number of times = once
 - · use of R-type flux

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