TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA8211AH

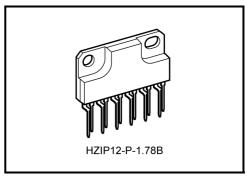
Dual Audio Power Amplifier

The TA8211AH is dual audio power amplifier for consumer applications.

This IC provides an output power of 6 watts per channel (at V_{CC} = 20 V, f = 1 kHz, THD = 10%, R_L = 8 Ω). It is suitable for power amplifier of TV and home stereo.

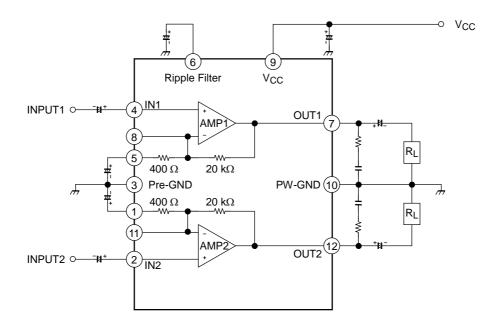
Features

- High output power: $P_{out} = 6$ W/channel (Typ.) ($V_{CC} = 20$ V, $R_L = 8$ Ω , f = 1 kHz, THD = 10%)
- Low noise: V_{no} = 0.14 mVrms (Typ.) (V_{CC} = 28 V, R_L = 8 Ω , G_V = 34dB, R_g = 10 k Ω , BW = 20 Hz~20 kHz)
- Very few external parts
- Built in thermal shut down protector circuit
- Operating supply voltage range: $V_{CC \text{ (opr)}} = 10 \sim 30 \text{ V (Ta} = 25 \text{°C)}$



Weight: 4.04 g (typ.)

Block Diagram



Application Information

Voltage gain

The closed loop voltage gain is determined by R1, R2.

$$\begin{split} G_V &= 20 \ell og \, \frac{R_1 + R_2}{R_2} (dB) \\ &= 20 \ell og \, \frac{20 \, k\Omega + 400 \, \Omega}{400 \, \Omega} \\ &\simeq \, 34 \, (dB) \end{split}$$

- (a) Amplifier with gain > 34dB $G_V = 20 log \frac{R_1 + R_2 /\!/ R_3}{R_2 /\!/ R_3} (dB)$ When $R_3 = 400 \Omega$ $G_V \simeq 40 (dB)$ is given.
- (b) Amplifier with gain < 34dB $G_V = 20 log \frac{R_1 + R_2 + R_4}{R_2 + R_4} (dB)$ When $R_4 = 220 \Omega$ $G_V \simeq 30 (dB)$ is given.

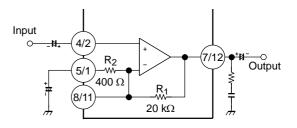


Figure 1

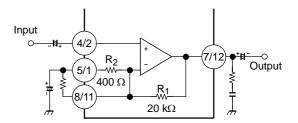


Figure 2

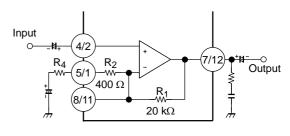


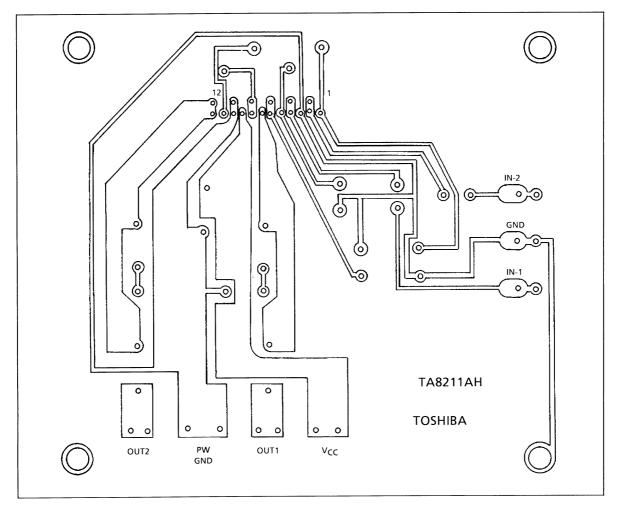
Figure 3

Cautions

This IC is not proof enough against a strong E-M field by CRT which may cause malfunction such as leak. Please set the IC keeping the distance from CRT.

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Standard PCB



(Bottom view)

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Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	30	٧
Output current (Peak/ch)	I _{O (peak)}	2	Α
Power dissipation	P _D (Note)	25	W
Operating temperature	T _{opr}	-20~75	°C
Storage temperature	T _{stg}	-55~150	°C

Note: Derated above $Ta = 25^{\circ}C$ in the proportion of 200 mW/°C.

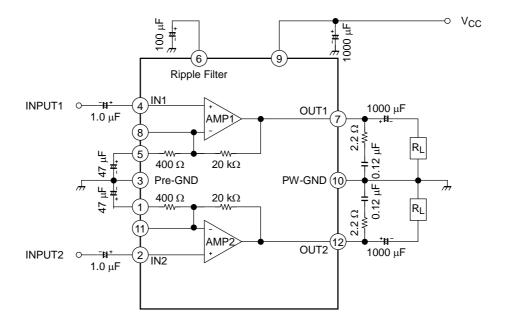
Electrical Characteristics (unless otherwise specified, V_{CC} = 20 V, R_L = 600 Ω , R_g = 600 Ω , f = 1 kHz, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Quiescent current	Iccq —		$V_{in} = 0$	_	75	130	mA	
Output power	Pout (1)	_	THD = 10%	5.0	6.0	_	W	
Output power	Pout (2)	_	THD = 1%	_	4.5	_	V V	
Total harmonic distortion	THD	_	P _{out} = 2 W	_	0.1	0.6	%	
Closed loop voltage gain	G _V	_	V _{out} = 0.775 Vrms (0dBm)	32.5	34.0	35.5	dB	
Open loop voltage gain	G _{VO}	_		_	60	_	dB	
Input resistance	R _{IN}	_	_	_	30	_	kΩ	
Ripple rejection ratio	R.R.	_	Rg = 0, f _{ripple} = 100 Hz V _{ripple} = 0.775 Vrms (0dBm)	-45	-57	_	dB	
Output noise voltage	V _{no}	_	Rg = 10 kΩ, BW = 20 Hz~20 kHz	_	0.14	0.3	mVrms	

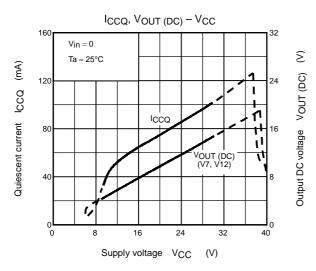
Typ. DC Voltage of Each Terminal ($V_{CC} = 20 \text{ V}$, $Ta = 25^{\circ}\text{C}$)

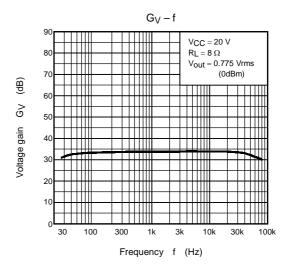
Terminal No.	1	2	3	4	5	6	7	8	9	10	11	12
DC voltage (V)	2.1	2.25	GND	2.25	2.1	6.8	9.8	2.25	V _{CC}	GND	2.25	9.8

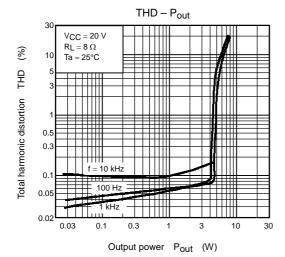
Test Circuit

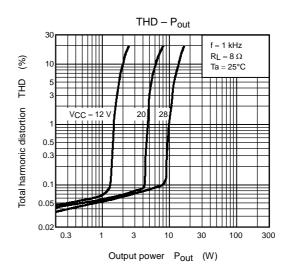


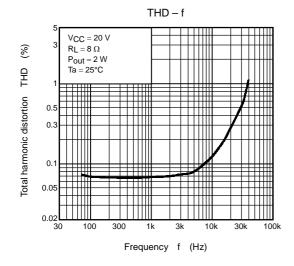
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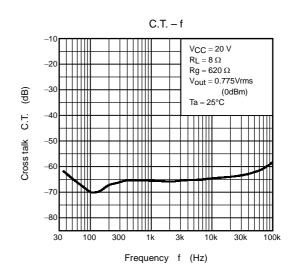


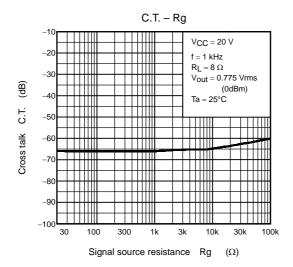


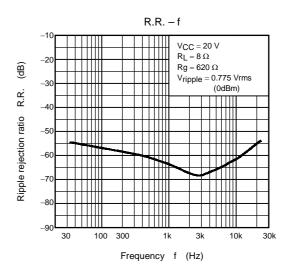


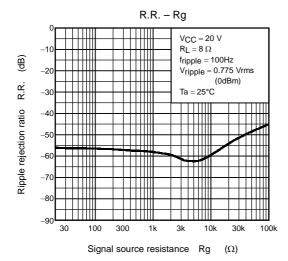


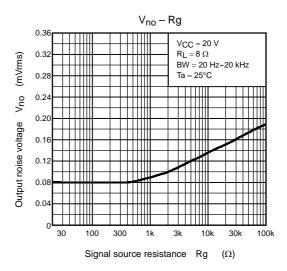


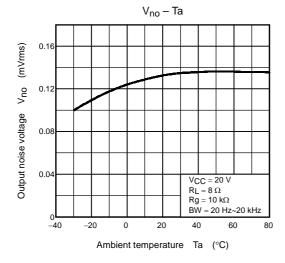


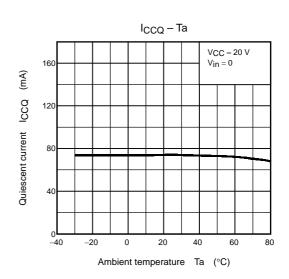


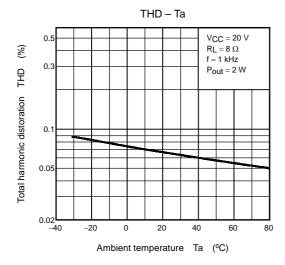


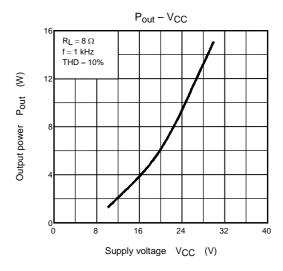


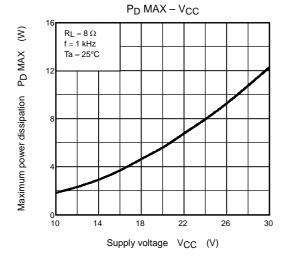


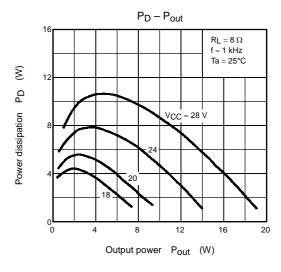


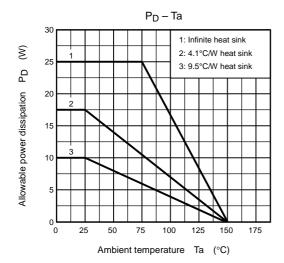










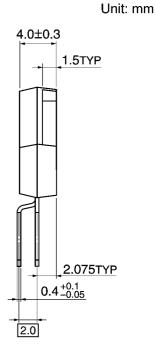


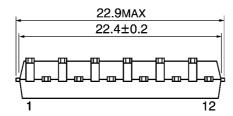
Package Dimensions

HZIP12-P-1.78B

15.72±0.2 ø3.6±0.2 14.8±0.3 11.31±0.3 1.45±0.2 0.55±0.1 0.25M 1.42TYP

1.1±0.1





1.778

Weight: 4.04 g (typ.)

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