

DDTA (R1 = R2 SERIES) UA

PNP PRE-BIASED SMALL SIGNAL SOT-323
SURFACE MOUNT TRANSISTOR

NEW PRODUCT

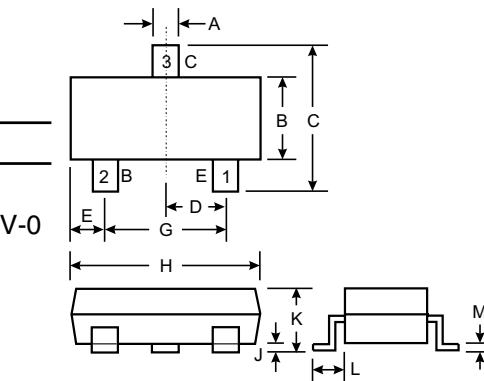
Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1 = R2

Mechanical Data

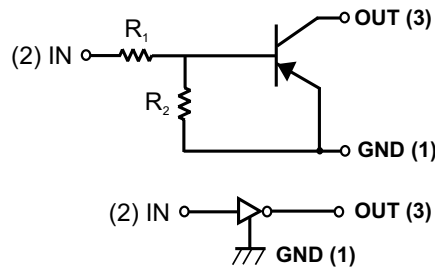
- Case: SOT-323, Molded Plastic
- Case material - UL Flammability Rating 94V-0
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approx.)

UNDER DEVELOPMENT



SOT-323		
Dim	Min	Max
A	0.37	0.51
B	1.19	1.40
C	2.10	2.50
D	0.89	1.05
E	0.45	0.61
G	1.78	2.05
H	2.65	3.05
J	0.013	0.15
K	0.89	1.10
L	0.45	0.61
M	0.076	0.178
All Dimensions in mm		

P/N	R1, R2 (NOM)	MARKING
DDTA123EUA	2.2K	P04
DDTA143EUA	4.7K	P08
DDTA114EUA	10K	P13
DDTA124EUA	22K	P17
DDTA144EUA	47K	P20
DDTA115EUA	100K	P24



SCHEMATIC DIAGRAM

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (1)	V _{CC}	-50	V
Input Voltage, (2) to (1)	V _{IN}	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V
Output Current	I _O	50	mA
Output Current	I _O	-100 -100 -50 -30 -30 -20	mA
Output Current	I _C (Max)	-100	mA
Power Dissipation	P _d	200	mW
Operating and Storage and Temperature Range	T _j , T _{STG}	-55 to +150	°C

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage		$V_{I(off)}$	-0.5	-1.1	—	V	$V_{CC} = 5V, I_O = 100\mu\text{A}$
		$V_{I(on)}$	—	-1.9	-3		$V_O = 0.3V, I_O = 20\text{mA}, \text{DDTA123EUA}$ $V_O = 0.3V, I_O = 20\text{mA}, \text{DDTA143EUA}$ $V_O = 0.3V, I_O = 10\text{mA}, \text{DDTA114EUA}$ $V_O = 0.3V, I_O = 5\text{mA}, \text{DDTA124EUA}$ $V_O = 0.3V, I_O = 2\text{mA}, \text{DDTA144EUA}$ $V_O = 0.3V, I_O = 1\text{mA}, \text{DDTA115EUA}$
Output Voltage		$V_{O(on)}$	—	-0.1	-0.3	V	$I_O/I_I = 10\text{mA}/0.5\text{mA}, \text{DDTA123EUA}$ $I_O/I_I = 10\text{mA}/0.5\text{mA}, \text{DDTA143EUA}$ $I_O/I_I = 10\text{mA}/0.5\text{mA}, \text{DDTA114EUA}$ $I_O/I_I = 10\text{mA}/0.5\text{mA}, \text{DDTA124EUA}$ $I_O/I_I = 10\text{mA}/0.5\text{mA}, \text{DDTA144EUA}$ $I_O/I_I = 5\text{mA}/0.25\text{mA}, \text{DDTA115EUA}$
Input Current	DDTA123EUA DDTA143EUA DDTA114EUA DDTA124EUA DDTA144EUA DDTA115EUA	I_I	—	—	-3.8 -1.8 -0.88 -0.36 -0.18 -0.15	mA	$V_I = -5V$
Output Current		$I_{O(off)}$	—	—	0.5	μA	$V_{CC} = -50V, V_I = 0V$
DC Current Gain	DDTA123EUA DDTA143EUA DDTA114EUA DDTA124EUA DDTA144EUA DDTA115EUA	G_I	-20 -20 -30 -56 -68 -82	—	—	—	$V_O = -5V, I_O = -20\text{mA}$ $V_O = -5V, I_O = -10\text{mA}$ $V_O = -5V, I_O = -5\text{mA}$ $V_O = -5V, I_O = -5\text{mA}$ $V_O = -5V, I_O = -5\text{mA}$ $V_O = -5V, I_O = -5\text{mA}$
Resistance Ratio		R_2/R_1	0.8	1	1.2	—	—
Gain-Bandwidth Product*		f_T	—	250	—	MHz	$V_{CE} = -10V, I_E = 5\text{mA},$ $f = 100\text{MHz}$

* Transistor - For Reference Only

UNDER DEVELOPMENT