

NEW PRODUCT

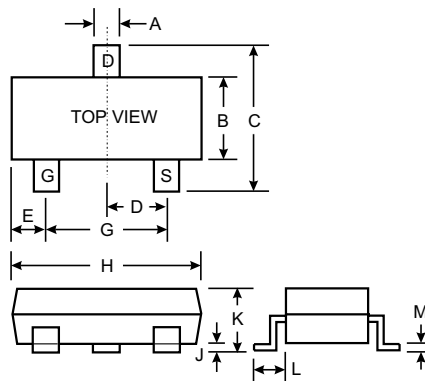
Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1≠R2

UNDER DEVELOPMENT

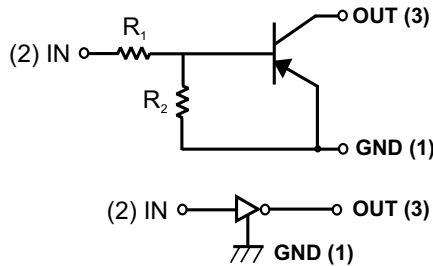
Mechanical Data

- Case: SC-59, Molded Plastic
- Case material - UL Flammability Rating 94V-0
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approx.)



SC-59		
Dim	Min	Max
A	0.30	0.50
B	1.40	1.80
C	2.50	3.00
D	0.85	1.05
E	0.30	0.70
G	1.70	2.10
H	2.70	3.10
J	—	0.10
K	1.00	1.40
L	0.55	0.70
M	0.10	0.35
All Dimensions in mm		

P/N	R1 (NOM)	R2 (NOM)	MARKING
DDTA113ZKA	1K	10K	P02
DDTA123YKA	2.2K	10K	P05
DDTA123JKA	2.2K	47K	P06
DDTA143XKA	4.7K	10K	P09
DDTA143FKA	4.7K	22K	P10
DDTA143ZKA	4.7K	47K	P11
DDTA114YKA	10K	47K	P14
DDTA114WKA	10K	4.7K	P15
DDTA124XKA	22K	47K	P18
DDTA144VKA	47K	10K	P21
DDTA144WKA	47K	22K	P22



SCHMATIC DIAGRAM

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (1)	V _{CC}	-50	V
Input Voltage, (2) to (1)	V _{IN}	+5 to -10 +5 to -12 +5 to -12 +7 to -20 +6 to -30 +5 to -30 +6 to -40 +10 to -30 +10 to -40 +15 to -40 +10 to -40	V
Output Current	I _O	-100 -100 -100 -100 -100 -100 -70 -100 -50 -30 -30	mA
Output Current	I _C (Max)	-100	mA
Power Dissipation	P _d	200	mW
Operating and Storage and Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition	
Input Voltage	DDTA113ZKA DDTA123YKA DDTA123JKA DDTA143XKA DDTA143FKA DDTA143ZKA DDTA114YKA DDTA114WKA DDTA124XKA DDTA144VKA DDTA144WKA	$V_{I(off)}$	-0.3 -0.3 -0.5 -0.3 -0.3 -0.5 -0.3 -0.8 -0.4 -1.0 -0.8	—	—		V	$V_{CC} = 5V, I_O = 100\mu A$
	DDTA113ZKA DDTA123YKA DDTA123JKA DDTA143XKA DDTA143FKA DDTA143ZKA DDTA114YKA DDTA114WKA DDTA124XKA DDTA144VKA DDTA144WKA	$V_{I(on)}$	—	—	-3.0 -3.0 -1.1 -2.5 -1.3 -1.3 -1.4 -3.0 -2.5 -5.0 -4.0		V	$V_O = -0.3V, I_O = -20mA$ $V_O = -0.3V, I_O = -20mA$ $V_O = -0.3V, I_O = -5mA$ $V_O = -0.3V, I_O = -20mA$ $V_O = -0.3V, I_O = -3mA$ $V_O = -0.3V, I_O = -5mA$ $V_O = -0.3V, I_O = -1mA$ $V_O = -0.3V, I_O = -2mA$ $V_O = -0.3V, I_O = -2mA$ $V_O = -0.3V, I_O = -2mA$ $V_O = -0.3V, I_O = -2mA$
Output Voltage		$V_{O(on)}$	—	-0.1	-0.3	V	$I_O/I_I = -5mA/-0.25mA$ DDTA123JKA $I_O/I_I = -5mA/-0.25mA$ DDTA143ZKA $I_O/I_I = -5mA/-0.25mA$ DDTA114YKA $I_O/I_I = -10mA/-0.5mA$ All Others	
Input Current	DDTA113ZKA DDTA123YKA DDTA123JKA DDTA143XKA DDTA143FKA DDTA143ZKA DDTA114YKA DDTA114WKA DDTA124XKA DDTA144VKA DDTA144WKA	I_I	—	—	-7.2 -3.8 -3.6 -1.8 -1.8 -1.8 -0.88 -0.88 -0.36 -0.16 -0.16	mA	$V_I = -5V$	
Output Current		$I_{O(off)}$	—	—	-0.5	μA	$V_{CC} = -50V, V_I = 0V$	
DC Current Gain	DDTA113ZKA DDTA123YKA DDTA123JKA DDTA143XKA DDTA143FKA DDTA143ZKA DDTA114YKA DDTA114WKA DDTA124XKA DDTA144VKA DDTA144WKA	G_I	-33 -33 -80 -30 -68 -80 -68 -24 -68 -33 -56	—	—	—	$V_O = -5V, I_O = -10mA$	
Gain-Bandwidth Product*		f_T	—	250	—	MHz	$V_{CE} = -10V, I_E = 5mA,$ $f = 100MHz$	

* Transistor - For Reference Only

UNDER DEVELOPMENT