

Mask ROM number	
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**740 FAMILY MASK ROM CONFIRMATION FORM  
SINGLE-CHIP MICROCOMPUTER M37210M3-XXXSP/FP  
MITSUBISHI ELECTRIC**

Receipt	Date:	
	Section head signature	Supervisor signature

Note : Please fill in all items marked \*.

* Customer	Company name	TEL (      )	Issuance signature	Submitted by	Supervisor
	Date issued	Date :			

\*1. Confirmation

Specify the name of the product being ordered.

Three EPROMs are required for each pattern if this order is performed by EPROMs.

One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name :       M37210M3-XXXSP       M37210M3-XXXFP

Ordering by EPROMs

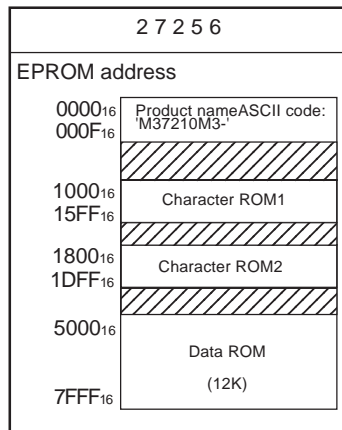
If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM 

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 (hexadecimal notation)

EPROM type (indicate the type used)



(1) Set "FF<sub>16</sub>" in the shaded area.

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**mitsubishi electric**

(2) Write the ASCII codes that indicate the product name of "M37210M3-" to addresses 0000<sub>16</sub> to 000F<sub>16</sub>.

Addresses 0000<sub>16</sub> to 000F<sub>16</sub> store the product name.  
 ASCII codes 'M37210M3-' are listed on the right.  
 The addresses and data are in hexadecimal notation.

Address		Address	
0000 <sub>16</sub>	'M' = 4D <sub>16</sub>	0008 <sub>16</sub>	'-' = 2D <sub>16</sub>
0001 <sub>16</sub>	'3' = 33 <sub>16</sub>	0009 <sub>16</sub>	FF <sub>16</sub>
0002 <sub>16</sub>	'7' = 37 <sub>16</sub>	000A <sub>16</sub>	FF <sub>16</sub>
0003 <sub>16</sub>	'2' = 32 <sub>16</sub>	000B <sub>16</sub>	FF <sub>16</sub>
0004 <sub>16</sub>	'1' = 31 <sub>16</sub>	000C <sub>16</sub>	FF <sub>16</sub>
0005 <sub>16</sub>	'0' = 30 <sub>16</sub>	000D <sub>16</sub>	FF <sub>16</sub>
0006 <sub>16</sub>	'M' = 4D <sub>16</sub>	000E <sub>16</sub>	FF <sub>16</sub>
0007 <sub>16</sub>	'3' = 33 <sub>16</sub>	000F <sub>16</sub>	FF <sub>16</sub>

**Note:** If the name of the product contained in the EPROMs does not match the name on the mask ROM confirmation form, the ROM processing is disabled. Please make sure the data is written correctly.

Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

File code 

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 (hexadecimal notation)

Mask file name 

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 .MSK (equal or less than eight characters)

**\*2. Mark specification**

Mark specification must be submitted using the correct form for the type of package being ordered. Fill the appropriate mark specification form (52P4B for M37210M3-XXXSP , 64P6N for M37210M3-XXXFP) and attach to the mask ROM confirmation form.

**\*3. Comments**

## 740 FAMILY MASK ROM CONFIRMATION FORM

### SINGLE-CHIP MICROCOMPUTER M37210M3-XXXSP/FP

#### MITSUBISHI ELECTRIC

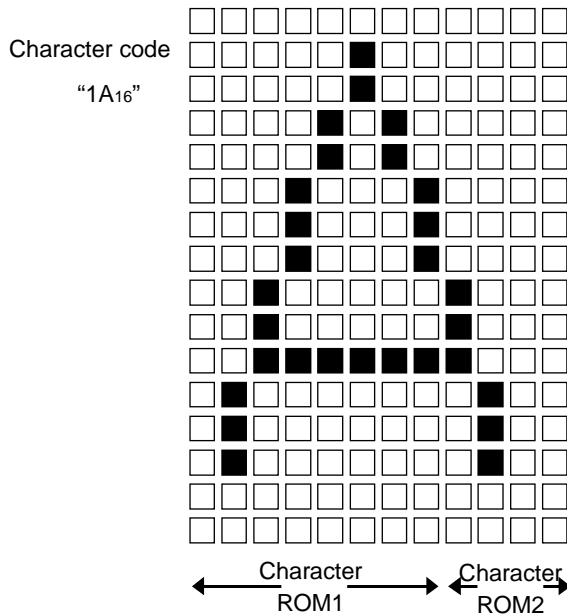
**Inputting the character ROM**

Input the character ROM data by dividing it into character ROM1 and character ROM2.

As for EPROM, the character ROM data must be input, starting from address 1XXX<sub>16</sub>; as for floppy disk, the character ROM data must be input, starting from address 3XXX<sub>16</sub>.

The structure of character ROM(divided into 12 X 16 dots font)

Example . As for EPROM



	b7	b6	b5	b4	b3	b2	b1	b0	
0									00 <sub>16</sub>
1									04 <sub>16</sub>
2									04 <sub>16</sub>
3									0A <sub>16</sub>
4									0A <sub>16</sub>
5									11 <sub>16</sub>
6									11 <sub>16</sub>
7									11 <sub>16</sub>
8									20 <sub>16</sub>
9									20 <sub>16</sub>
A									3F <sub>16</sub>
B									40 <sub>16</sub>
C									40 <sub>16</sub>
D									40 <sub>16</sub>
E									00 <sub>16</sub>
F									00 <sub>16</sub>

Example 11A0<sub>16</sub>  
to  
11AF<sub>16</sub>

	b7	b6	b5	b4	b3	b2	b1	b0	
0									F0 <sub>16</sub>
1									F0 <sub>16</sub>
2									F0 <sub>16</sub>
3									F0 <sub>16</sub>
4									F0 <sub>16</sub>
5									F0 <sub>16</sub>
6									F0 <sub>16</sub>
7									F0 <sub>16</sub>
8									F8 <sub>16</sub>
9									F8 <sub>16</sub>
A									F8 <sub>16</sub>
B									F4 <sub>16</sub>
C									F4 <sub>16</sub>
D									F4 <sub>16</sub>
E									F0 <sub>16</sub>
F									F0 <sub>16</sub>

Example 19A0<sub>16</sub>  
to  
19AF<sub>16</sub>