

50V/15A Switching Applications

Applications

· Relay drivers, high-speed inverters, converters, and other general high-current switching applications.

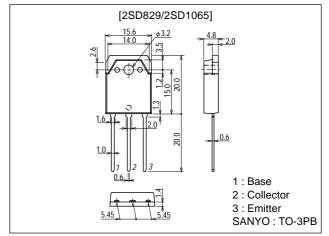
Features

- \cdot Low-saturation collector-to-emitter voltage : $V_{CE(sat)}$ =–0.5V max.
- · Wide ASO leading to high resistance to breakdown.

Package Dimensions

unit:mm

2022A



(): 2SB829

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(–)60	V
Collector-to-Emitter Voltage	VCEO		(-)50	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	lc		(–)15	Α
Collector Current (Pulse)	ICP		(-)20	Α
Collector Dissipation	PC	Tc=25°C	90	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0			(-)0.1	mA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)1A	70*		280*	
DC Current Gain	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)8A	30			
Gain-Bandwidth Product	f _T	V _{CE} =(-)5V, I _C =(-)1A		20		MHz
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)8A, I _B =(-)0.4A		(-0.26)	(-0.5)	V
				0.18	0.4	V

 $[\]overline{*}$: The 2SB829/2SD1065 are classified by 1A h_{FE} as follows:

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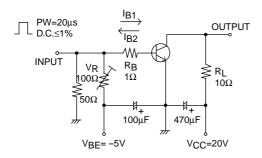
R	ank	Q	R	S
h	FE	70 to 140	100 to 200	140 to 280

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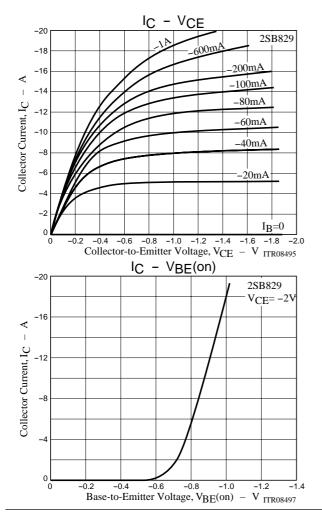
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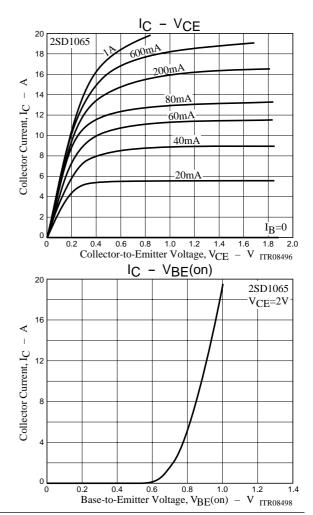
Parameter	Symbol	Conditions	Ratings			Unit
Faianielei	Symbol		min	typ	max	Offic
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =(-)1mA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	I _C =(−)1mA, R _{BE} =∞	(-)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)1mA, I _C =0	(–)6			٧
Turn-ON Time	ton	See specified Test Circuit		0.2		μs
Fall Time	t _f	See specified Test Circuit		(0.5)		μs
I all tille				1.0		μs
Storage Time	t _{stg}	See specified Test Circuit	·	0.1		μs

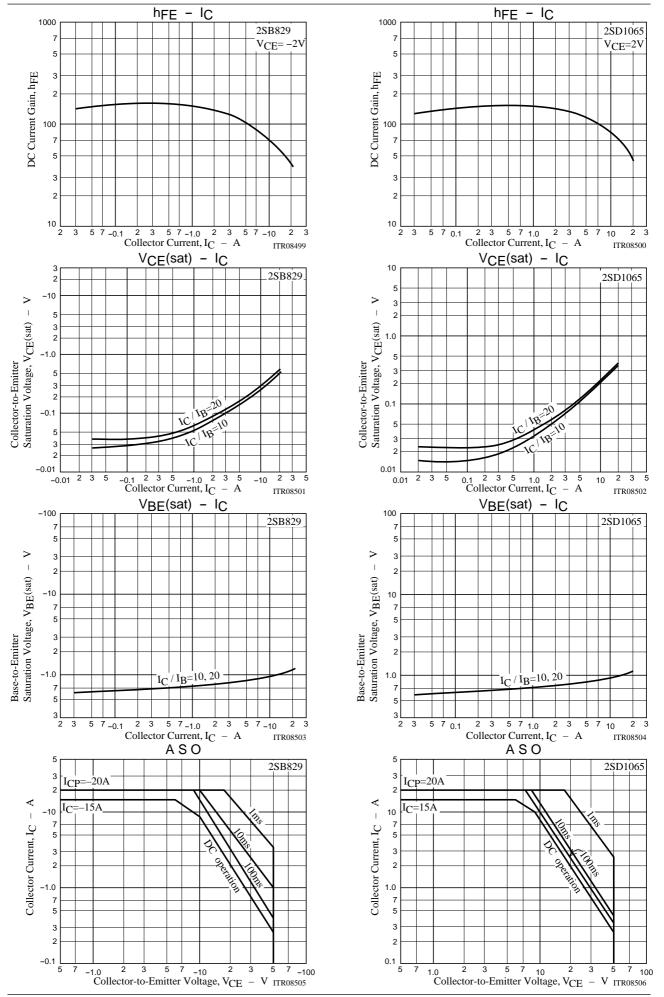
Switching Time Test Circuit

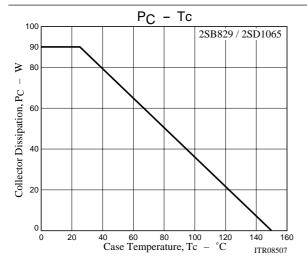


 I_{C} =10 I_{B1} = -10 I_{B2} =2A (For PNP, the porarity is reversed.)









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