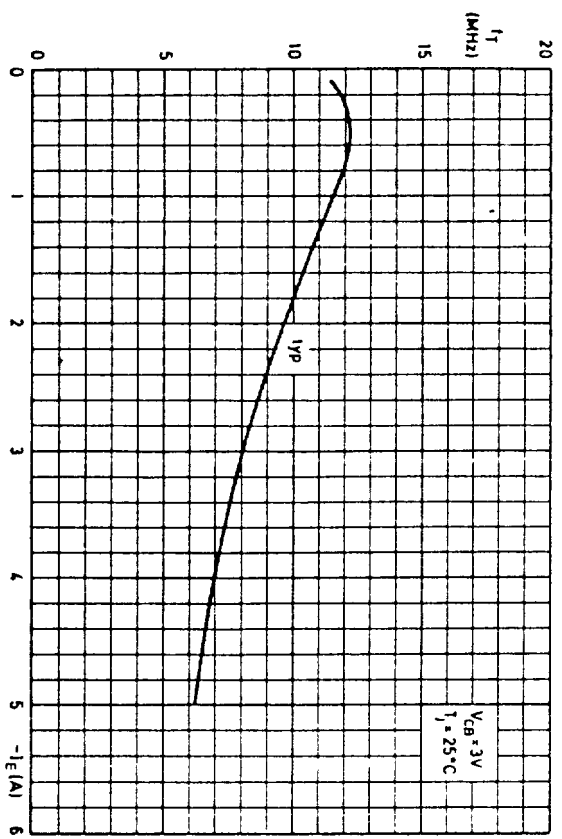
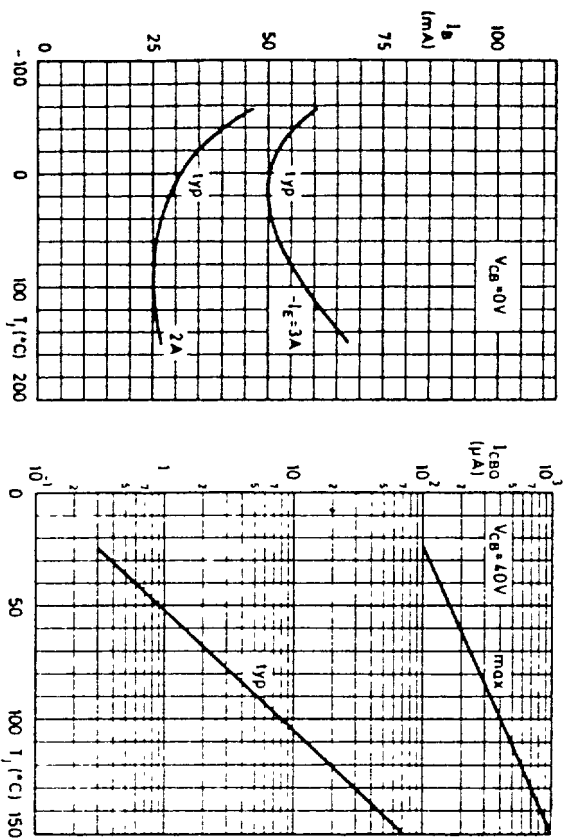


**N-P-N SILICON EPITAXIAL-BASE
A.F. POWER TRANSISTORS**

**BD201
BD203**

N-P-N silicon epitaxial-base power transistors in a plastic envelope. With their p-n-p complements BD202 and BD204 they are primarily intended for use in hi fi equipment delivering an output of 15 to 25W into 4 or 8Ω load



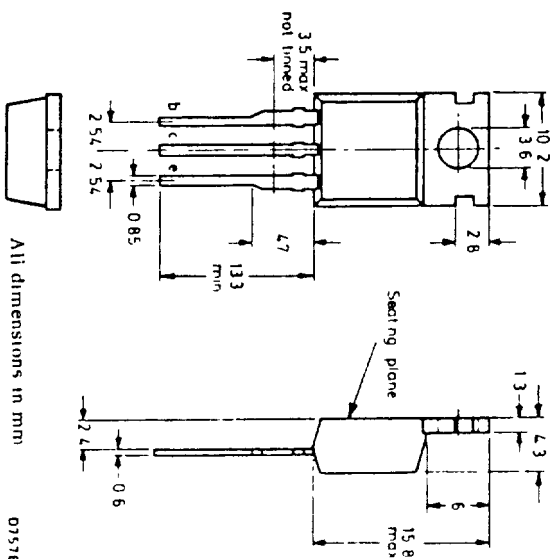
QUICK REFERENCE DATA

	BD201	BD203	
V_{CEO} max.	60	60	V
V_{CE0} max.	45	60	V
V_{CE0} max.	8.0	8.0	A
I_C max.	60	60	W
P_{tot} max. ($T_{mh} \leq 25^\circ C$)	150	150	$^\circ C$
f_1 max.	30	-	
h_{FE} min. ($I_C = 3A$, $V_{CE} = 2V$)	-	30	
h_{FE} min. ($I_C = 2A$, $V_{CE} = 2V$)	-	30	
h_{FE} min. ($I_C = 0.3A$, $V_{CE} = 3V$)	25	25	A:1:2

(Unless otherwise stated data are applicable to both types)

OUTLINE AND DIMENSIONS Similar to J.E.D.E.C. TO-220

Collector connected to the metal part of the mounting surface.



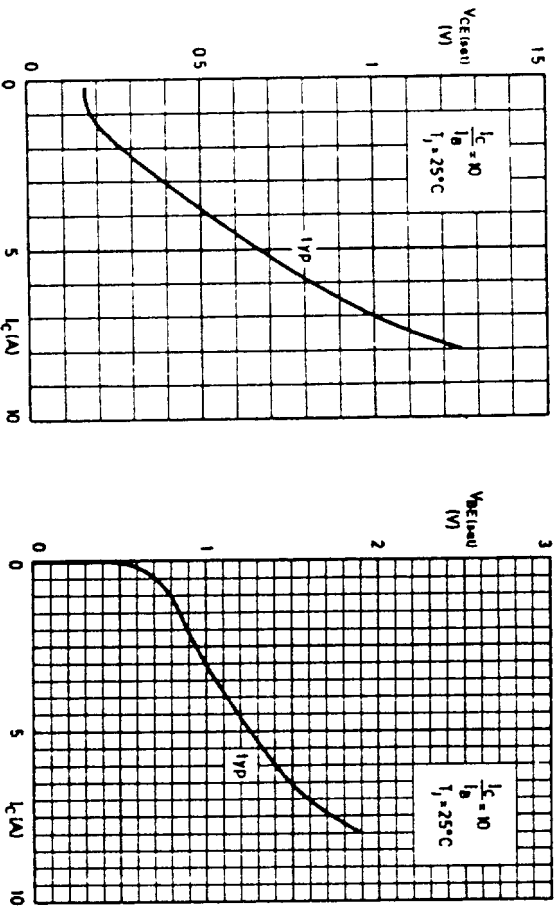
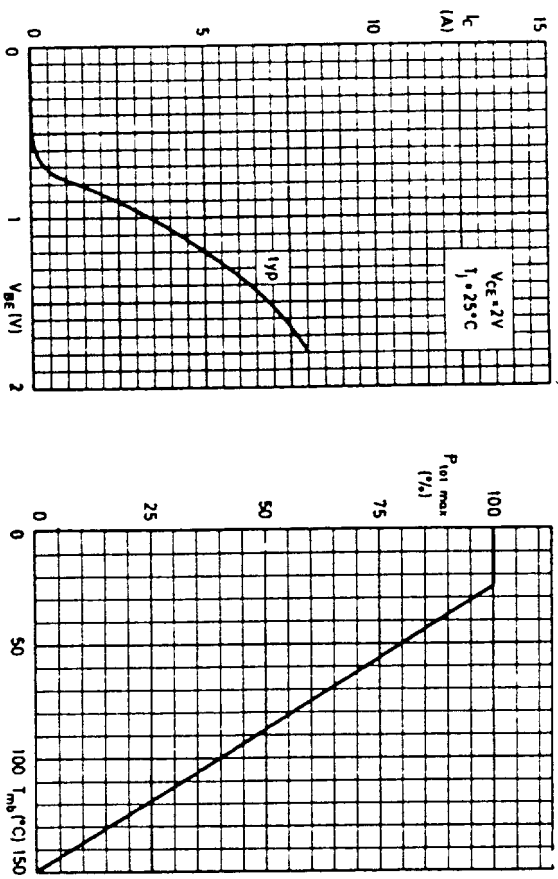
All dimensions in mm

07576

Accessories available 56338 (Insulating bush), 56325 (mica washer)

N-P-N SILICON EPITAXIAL-BASE A.F. POWER TRANSISTORS

BD201
BD203



RATINGS

Limiting values of operation according to the absolute maximum system

Electrical	BD201	BD203	
V_{CE0} max.	60	60	V
V_{CE0} max.	45	60	V
V_{BE0} max.	5.0	5.0	V
I_C max. (d.c.)	8.0	8.0	A
I_{CM} max. (peak value, $t_p \leq 10ms$)	12	12	A
I_{CSM} max. (non-repetitive peak value, $t_p \leq 2ms$)	25	25	A
P_{tot} max. ($T_j = 25^\circ C$)	60	60	W

Temperature

T_{sig}	65 to +150	$^\circ C$
T_j max.	150	$^\circ C$

THERMAL CHARACTERISTICS

$R_{th(j-c)}$	2.08	$^\circ C/W$
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ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ C$ unless otherwise stated)

Min. Typ. Max.

I_{CEO}	$I_B = 0, V_{CE} = 30V$	-	1.0	mA
I_{CBO}	$I_E = 0, V_{CB} = 40V, T_j = 150^\circ C$	-	1.0	mA
I_{EBO}	Emitter cut-off current	-	5.0	mA
V_{BE}	$I_C = 0, V_{EB} = 5V$	-	1.5	V
V_{BE}	Base-emitter voltage	-	1.5	V

$V_{CE(sat)}$

$I_C = 3A, I_B$ = the value for which $I_C = 3A$ at $V_{CE} = 2V$

$V_{CE(sat)}$

Collector-emitter saturation voltage
 $I_C = 3A, I_B = 0.3A$

Measured with pulses of 300 μs and with 2% duty cycle.

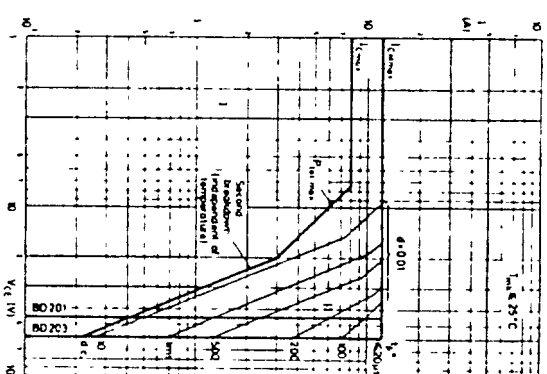
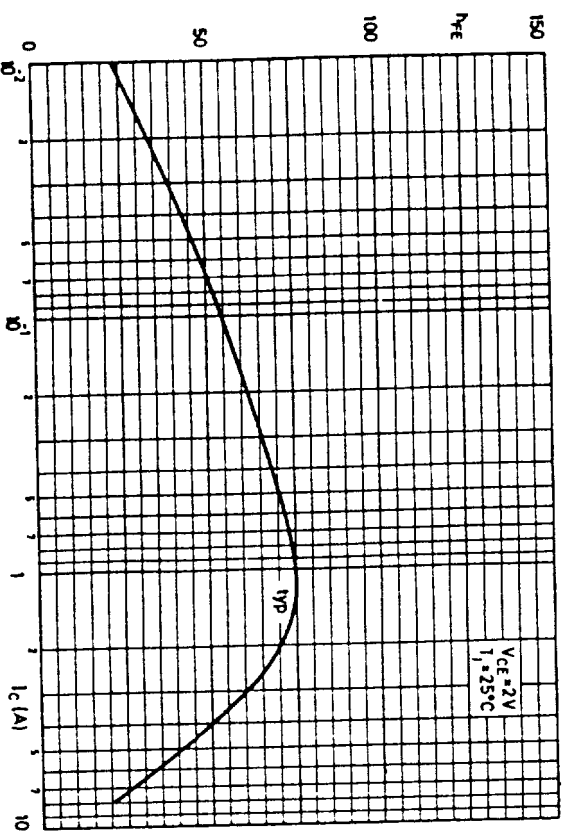
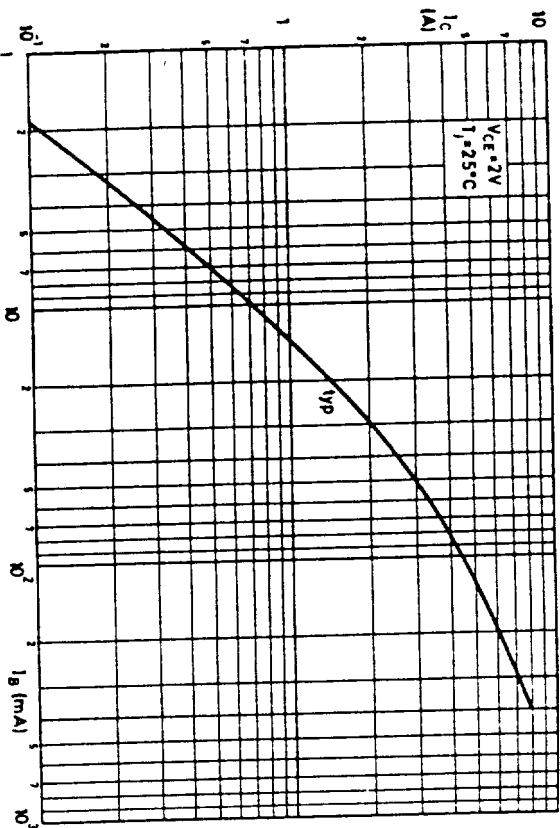
N-P-N SILICON EPITAXIAL-BASE A.F. POWER TRANSISTORS

**BD201
BD203**

ELECTRICAL CHARACTERISTICS (cont'd)

*h _{FE}	Static forward current transfer ratio	Min.	Typ.	Max.
	I _C = 1A, V _{CE} = 2V	30	-	-
	I _C = 3A, V _{CE} = 2V	BD201	30	-
	I _C = 2A, V _{CE} = 2V	BD203	-	-
f _{hfc}	Cut-off frequency	25*	-	KHz
f _T	Transition frequency	3.0	-	MHz

* Measured with pulses of 300μs and with 2% duty cycle.



SAFE OPERATING AREAS WITH THE TRANSISTOR FOR FORWARD BIASED

- I Region of permissible d. c. operation
- II Permissible extension for repetitive pulse operation (for P_{TOT} max. versus T_{mid} see page 7)

**N-P-N SILICON EPITAXIAL-BASE
A.F. POWER TRANSISTORS**

**BD201
BD203**

