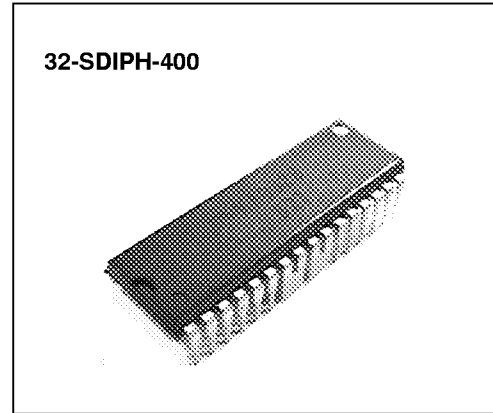


### 3-PHASE CAPSTAN MOTOR DRIVER

The KA8329B is a monolithic integrated circuit, and it is suitable for 3-phase capstan motor driver of VCR system.

### FEATURES

- 3-phase, full-wave, linear BLDC motor driver with 3 hall sensors
- Built-in TSD (Thermal shutdown) circuit
- Built-in torque ripple control circuit
- Built-in output current limiter
- Motor speed control
- High output current
- Built-in FG amplifier with sinusoidal waveforms
- Built-in hall amplifier
- Built-in CW and CCW circuit



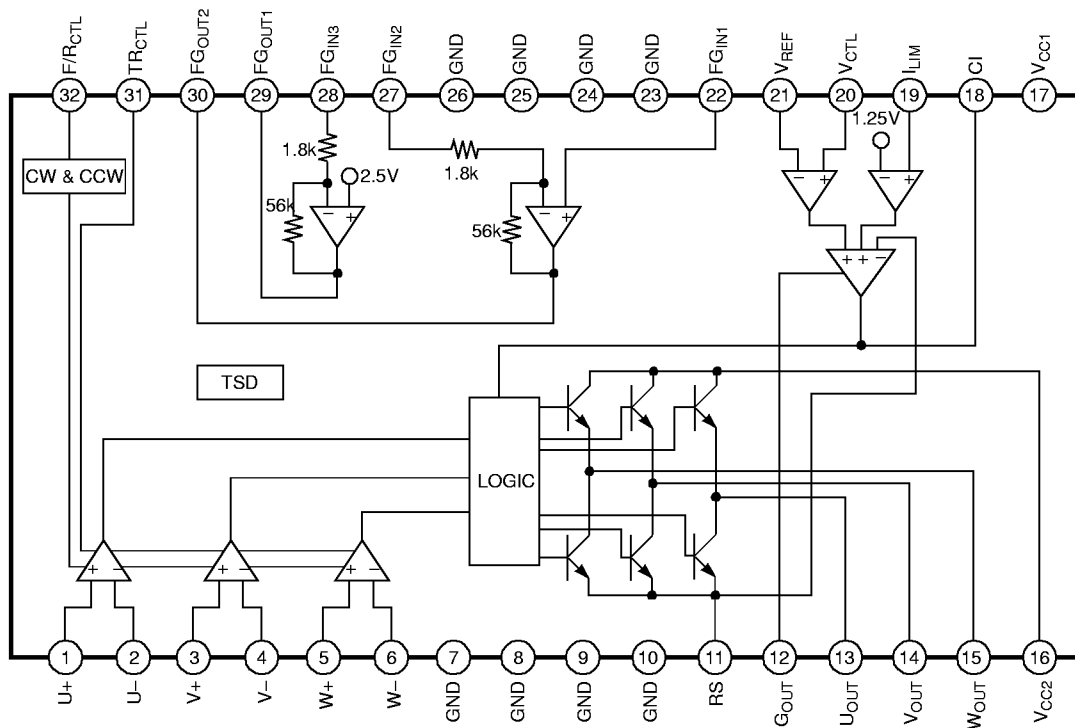
### ORDERING INFORMATION

Device	Package	Operating Temperature
KA8329B	32-SDIPH-400	-25°C ~ +75°C

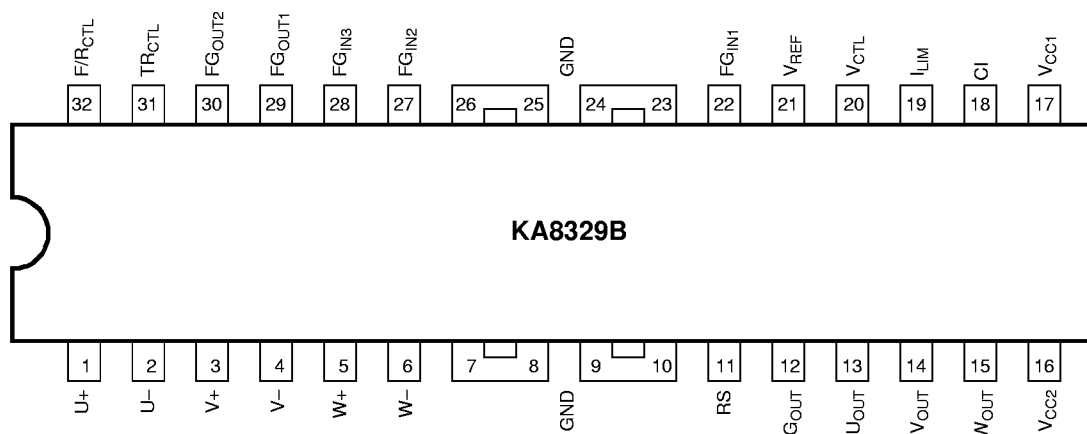
### TARGET APPLICATION

- VCR capstan motors

### BLOCK DIAGRAM



## PIN CONFIGURATIONS



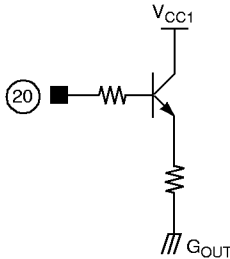
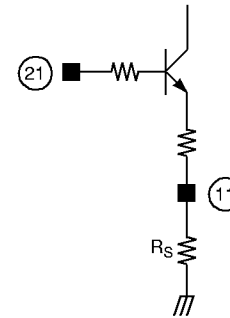
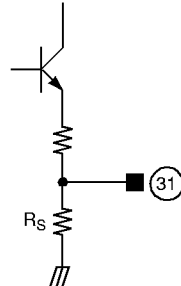
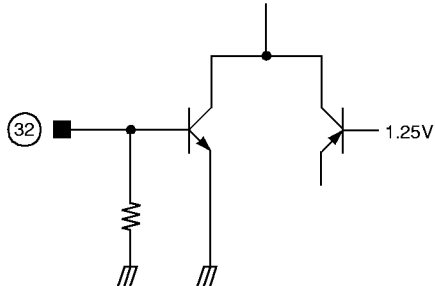
## PIN DESCRIPTION

Pin No.	Symbol	I/O	Description	Pin No.	Symbol	I/O	Description
1	U+	I	U+ hall signal input	17	V <sub>CC1</sub>	-	Supply voltage
2	U-	I	U- hall signal input	18	CI	-	Phase stabilization
3	V+	I	V+ hall signal input	19	I <sub>LIM</sub>	I	Current limitation
4	V-	I	V- hall signal input	20	V <sub>CTL</sub>	I	Voltage control
5	W+	I	W+ hall signal input	21	V <sub>REF</sub>	I	Voltage control reference
6	W-	I	W- hall signal input	22	FG <sub>IN1</sub>	I	FG amp1 input1
7	GND	-	Ground (Signal)	23	GND	-	Ground (Signal)
8	GND	-	Ground (Signal)	24	GND	-	Ground (Signal)
9	GND	-	Ground (Signal)	25	GND	-	Ground (Signal)
10	GND	-	Ground (Signal)	26	GND	-	Ground (Signal)
11	RS	O	Output current detection	27	FG <sub>IN2</sub>	I	FG amp1 input2
12	G <sub>OUT</sub>	-	Ground (Power)	28	FG <sub>IN3</sub>	I	FG amp2 input1
13	U <sub>OUT</sub>	O	U-phase output	29	FG <sub>OUT1</sub>	O	FG amp2 output
14	V <sub>OUT</sub>	O	V-phase output	30	FG <sub>OUT2</sub>	O	FG amp1 output
15	W <sub>OUT</sub>	O	W-phase output	31	TR <sub>CTL</sub>	I	Troque ripple control
16	V <sub>CC2</sub>	-	Supply voltage (Power)	32	F/R <sub>CTL</sub>	I	Forward & reverse control

INTERNAL CIRCUIT

Description	Pin No.	Internal circuit
Hall input	1, 2, 3 4, 5, 6	
Output & Current detection	13, 14, 15, 12	
Speed control (Current limitation)	19	

INTERNAL CIRCUIT (Continued)

Description	Pin No.	Internal circuit
Speed control (Voltage control)	20	
Voltage control reference	21	
Torque ripple control	31	
Forward & Reverse control	32	

INTERNAL CIRCUIT (Continued)

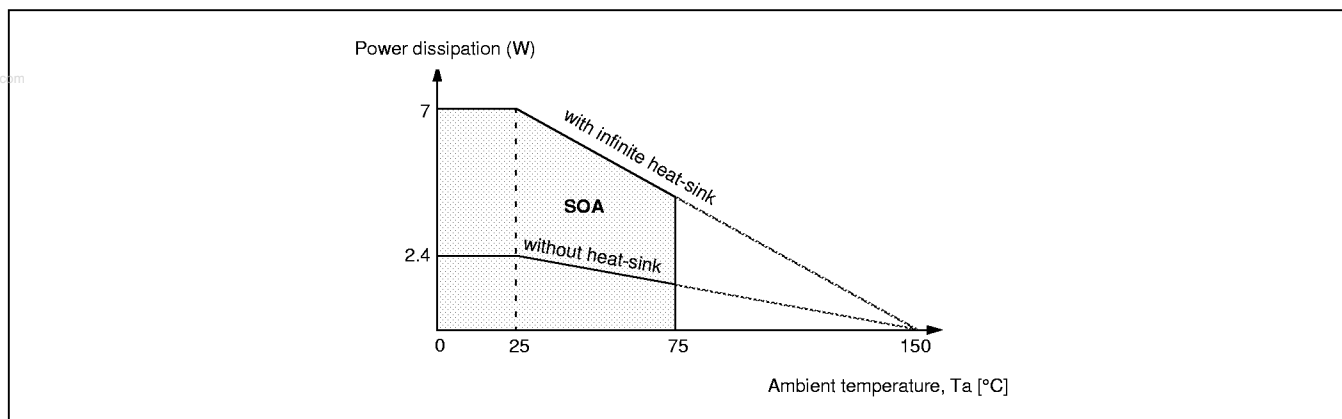
Description	Pin No.	Internal circuit
FG AMP.	28, 29 22, 27, 30	
Phase stabilization	16, 18	

**ABSOLUTE MAXIMUM RATING (Ta=25°C)**

Characteristics	Symbol	Value	Unit	Remark
Supply voltage (Signal)	$V_{CC1max}$	7	V	–
Supply voltage (Power)	$V_{CC2max}$	28	V	–
Output current	$I_{Omax}$	1.5 <sup>note1</sup>	A / Phase	–
Power dissipation	$P_d$	2.4 <sup>note2</sup>	W	No heat sink
Thermal resistance	$R_T$	60.2	mW / °C	No heat sink
Junction temperature	$T_J$	150	°C	–
Operating temperature	$T_{OPR}$	–25 ~ +75	°C	–
Storage temperature	$T_{STG}$	–40 ~ +125	°C	–

**NOTES:**

- Duty 1/100, pulse width 500 $\mu$ s
- When mounted on glass epoxy PCB (76.2 × 114 × 1.57mm)
  - Power dissipation reduces 19.2mW / °C for using above Ta=25°C. (Without heat-sink)
  - Do not exceed Pd and SOA.

**PD GRAPH****RECOMMENDED OPERATING CONDITIONS (Ta=25°C)**

Characteristics	Symbol	Value	Unit
Operating supply voltage (Signal)	$V_{CC1}$	4.5 ~ 5.5	V
Operating supply voltage (Power)	$V_{CC2}$	8 ~ 27	V

## ELECTRICAL CHARACTERISTICS

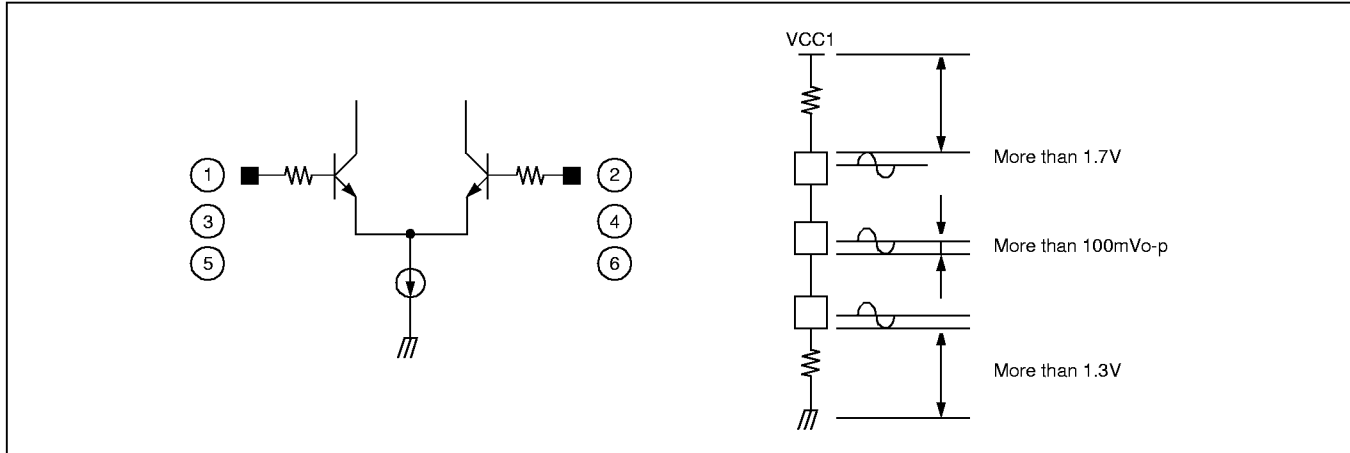
(Ta=25°C, V<sub>CC1</sub>=5V, V<sub>CC2</sub>=16V, R<sub>S</sub>=0.5Ω, unless otherwise specified)

Characteristic	Symbol	Pin No.	Test conditions	Min.	Typ.	max.	Unit
Quiescent input current	I <sub>CC1</sub>	17	V <sub>F/R</sub> =0V or 5V	5	8.5	12	mA
Hall amp. input voltage range	V <sub>INS</sub>	1 ~ 6	mV <sub>o-p</sub>	100	–	–	mV
Power TR stauration voltage (Outflow current)	V <sub>SAT1</sub>	16– 13, 14, 15	V <sub>CC2</sub> =13V, I <sub>OUT</sub> =0.8A/Phase	–	1.8	2.0	V
Power TR saturation voltage (Inflow current)	V <sub>SAT2</sub>	11– 13, 14, 15	V <sub>CC2</sub> =13V, I <sub>OUT</sub> =0.8A/Phase	–	1.8	2.0	V
I <sub>LIM</sub> input voltage range	V <sub>LIM</sub>	19	–	0	–	V <sub>CC1</sub>	V
I <sub>LIM</sub> input current	I <sub>19</sub>	19	V <sub>CTL</sub> =3.5V, V <sub>LIM</sub> =3V	–	350	2000	nA
I <sub>LIM</sub> current limit level	GML	19– 13, 14, 15	V <sub>CTL</sub> =3.5V, V <sub>LIM</sub> =Adjustable	0.61	0.67	0.73	A/V
I <sub>LIM</sub> quiescent output current	I <sub>O1</sub>	19	V <sub>LIM</sub> =0V	–	1.5	5.0	mA
I <sub>LIM</sub> limit offset voltage	V <sub>O1</sub>	19	V <sub>REF</sub> =2.5V	1.17	1.25	1.33	V
V <sub>CTL</sub> input voltage range	V <sub>CTL</sub>	20	–	0	–	V <sub>CC1</sub>	V
V <sub>CTL</sub> input current	I <sub>20</sub>	20	V <sub>CTL</sub> =3V, V <sub>LIM</sub> =5V	–	350	2000	nA
V <sub>CTL</sub> control gain	GM	20– 13, 14, 15	V <sub>CTL</sub> =3.5V, V <sub>LIM</sub> =Adjustable	0.9	1.0	1.1	A/V
V <sub>CTL</sub> quiescent output current	I <sub>O2</sub>	20	V <sub>CTL</sub> =0V	–	1.5	5.0	mA
V <sub>CTL</sub> input offset voltage	V <sub>O2</sub>	20	V <sub>CTL</sub> =Adjustable	–50	0	50	mV
Forward rotation mode	V <sub>F</sub>	32	–	1.0	1.3	1.6	V
Reverse rotation mode	V <sub>R</sub>	32	–	0	–	0.8	–
V <sub>REF</sub> input voltage range	V <sub>REF</sub>	21	–	2.0	–	V <sub>CC1</sub> – 2.0	V
FG <sub>AMP</sub> internal reference voltage	V <sub>28</sub>	28	–	2.2	2.5 –	2.8	V V
FG <sub>AMP</sub> input voltage range	FG <sub>IN</sub>	27, 28	Pin28=10kHz, 60mVp-p	1	–	4	–
FG <sub>AMP1</sub> gain	FG <sub>AV1</sub>	28, 29	Sinusoidal waveforms Pin27=10kHz, 60mVp-p	28	31	34	Times
FG <sub>AMP2</sub> gain	FG <sub>AV2</sub>	27 ~ 30	Sinusoidal waveforms Pin22=2.5V	28	31	34	Times

**APPLICATION INFORMATION**

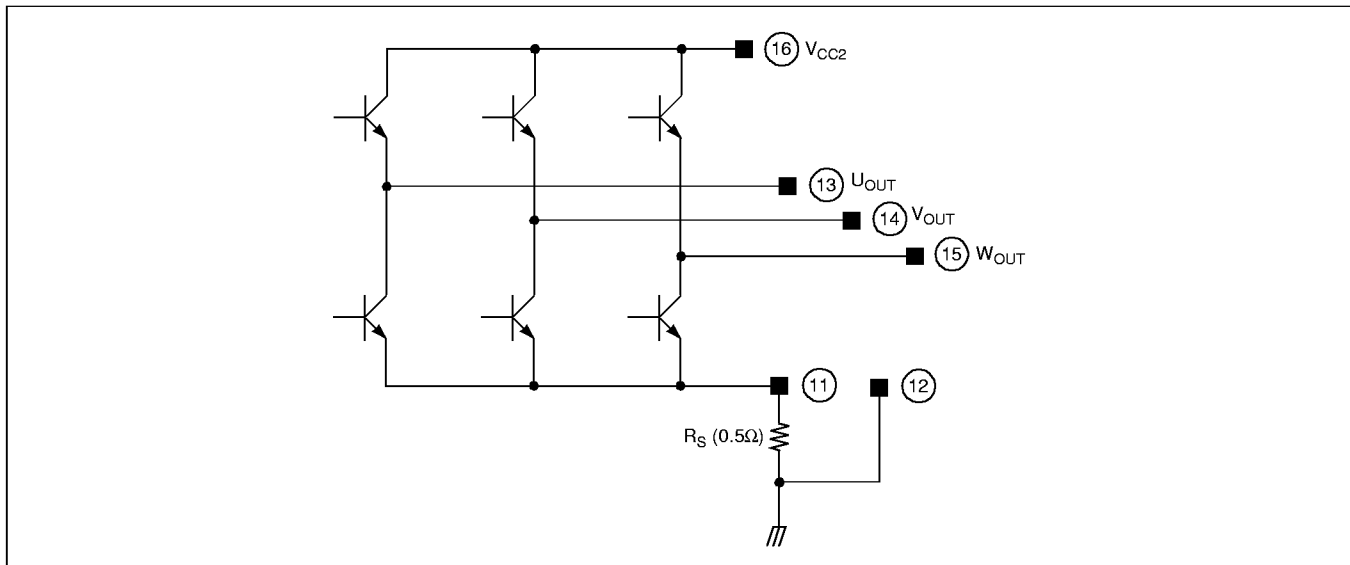
**1. HALL INPUT**

The input signal of the hall sensor requires more amplitude than 100mVo-p. and the operating voltage level of the hall sensor is from 1.2V ~  $V_{CC1}-0.8V$ .



**2. OUTPUT CURRENT DETECTION**

Pin 11 is usually connected with  $R_S$  (Approx.  $0.5\Omega$ ), and the motor current is converted to a voltage by the  $R_S$  and provided to a feedback amplifier. Pin 12 is connected to the circuit with the ground side or  $R_S$ .



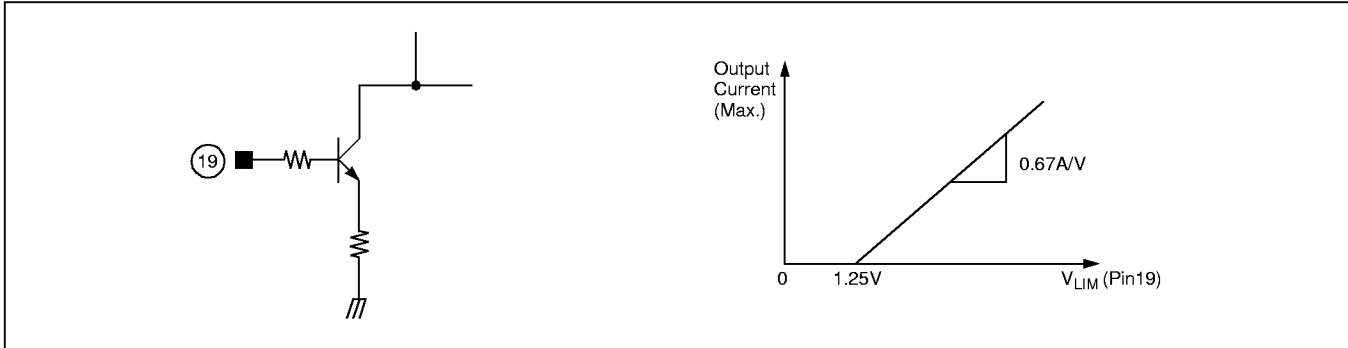


**3. MOTOR SPEED CONTROL (INPUT CURRENT LIMITATION)**

The maximum output current is limited by pin 19 voltage as follows. So a motor speed is controlled by the output current. In case of no-use, it is to be short-circuit with  $V_{CC1}$ .

$$GML = \Delta I_O / \Delta V_{LIM} = (I_{O2} - I_{O1}) / (V_{LIM2} - V_{LIM1}), \text{ where } V_{LIM1} = 1.45V \rightarrow \text{Output current} = I_{O1}$$

$$V_{LIM2} = 1.55V \rightarrow \text{Output current} = I_{O2}$$

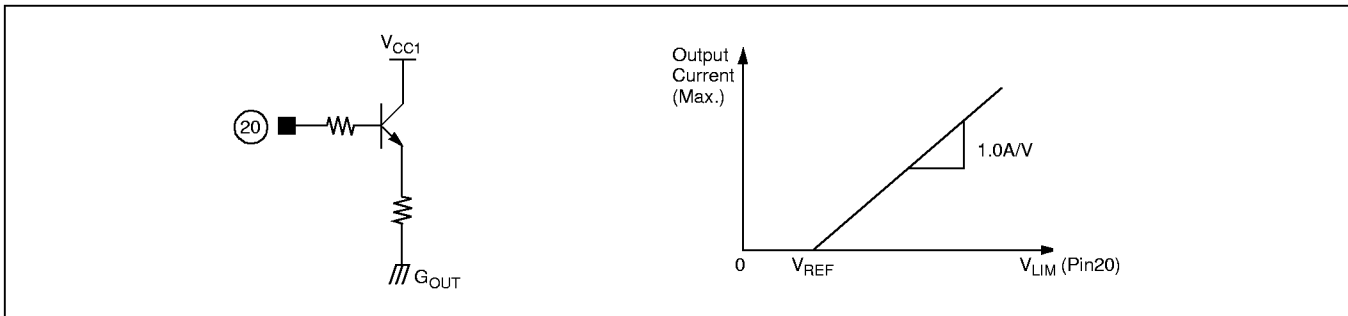


**4. MOTOR SPEED CONTROL (INPUT VOLTAGE CONTROL)**

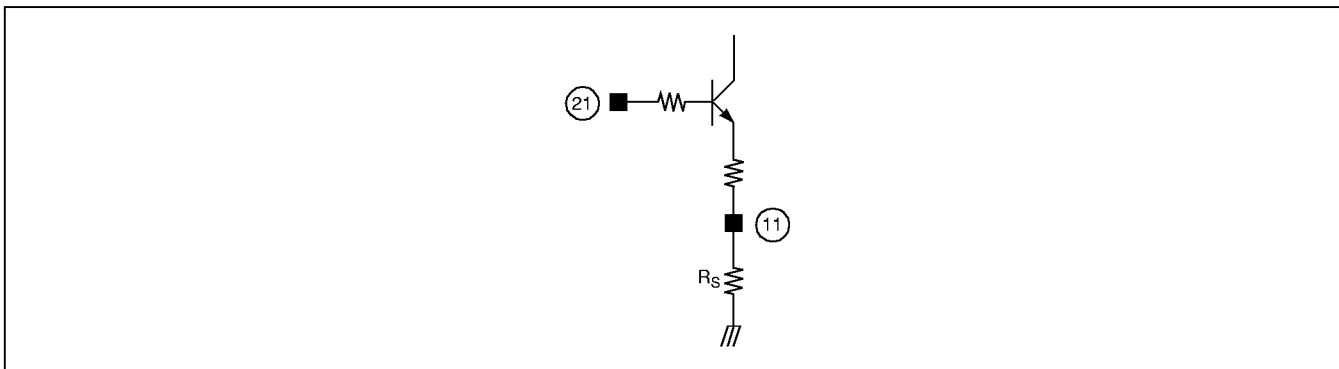
The control of motor speed is possible on the conditions of  $V_{CTL} (\text{Pin20}) \geq V_{REF}$ . The control gain is approx. 1.0A/V as follows.

$$GML = \Delta I_O / \Delta V_{CTL} = (I_{O2} - I_{O1}) / (V_{CTL2} - V_{CTL1}), \text{ where } V_{REF} = 2.5V, V_{CTL1} = 2.6V \rightarrow \text{Output current} = I_{O1}$$

$$V_{REF} = 2.5V, V_{CTL2} = 2.7V \rightarrow \text{Output current} = I_{O2}$$

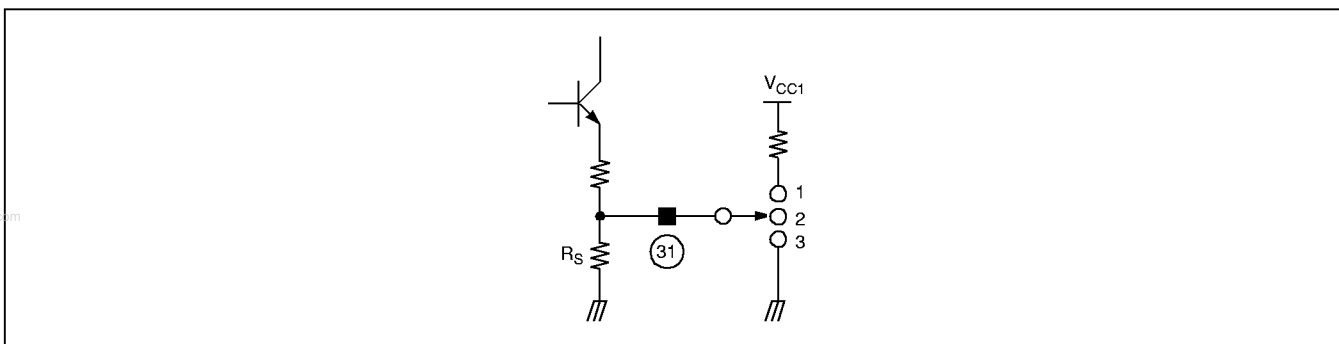


## 5. VOLTAGE CONTROL REFERENCE



The input voltage range is  $2V \leq V_{REF} \leq (V_{CC1} - 2V)$ .

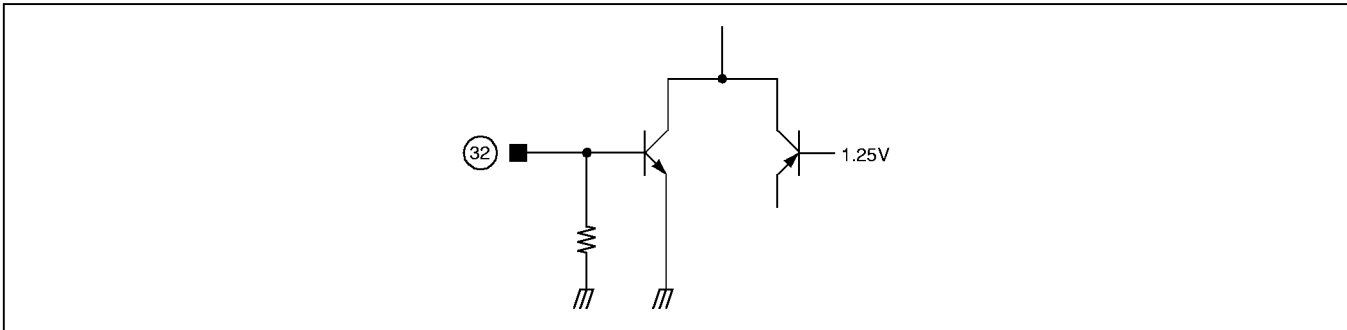
## 6. TORQUE RIPPLE CONTROL



The motor torque ripple is controlled by Pin 31 voltage as follows.

1. GND
2. Normal Mode
3. Control Mode

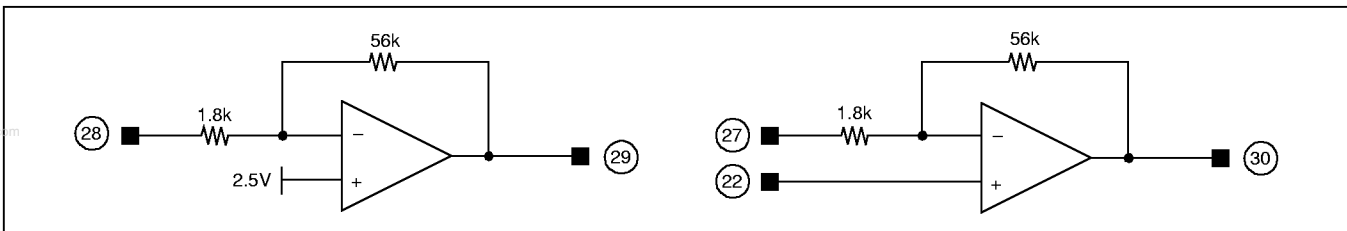
7. FORWARD & REVERSE ROTATION CONTROL



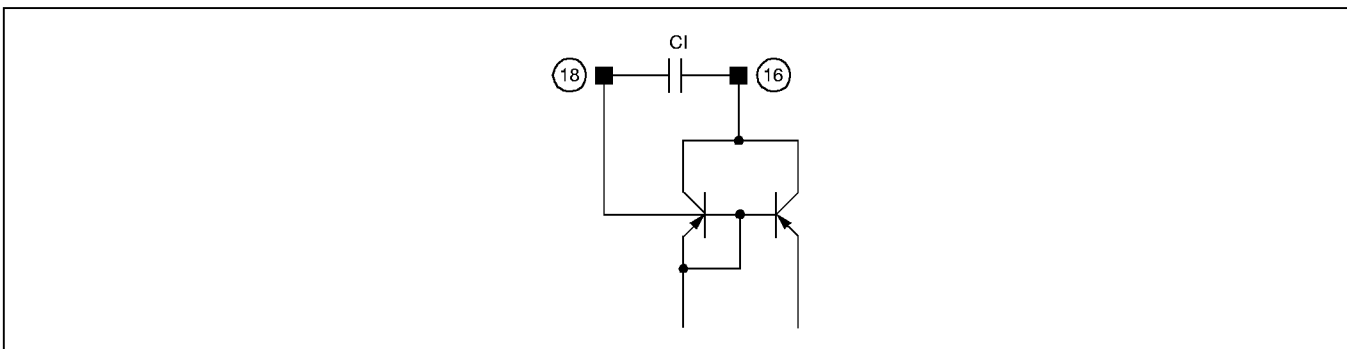
Forward mode: Pin 32  $\geq$  1.8V  
 Reverse mode: Pin 32  $\leq$  0.8V

8. FG AMP

These amplifiers are the inversion type. One amplifier is built in both the reference voltage (Approx. 2.5V) and the gain setting resistors. The voltage gain is approx. 31 times.

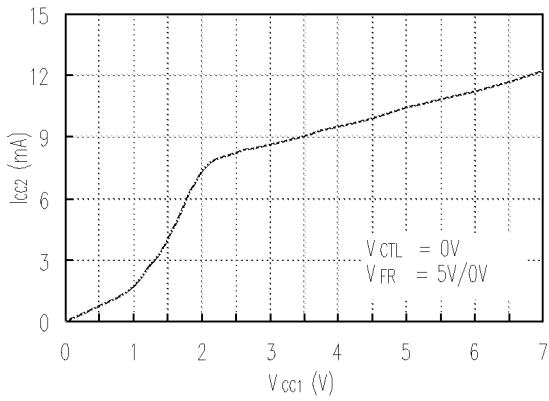


9. PHASE STABILIZATION

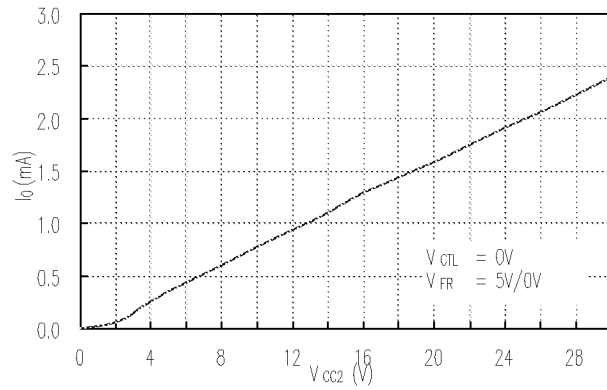


Be inserted a capacitor between pin 16 and pin 18. This capacitor, approx. 0.1 $\mu$ F is for the phase stabilization of the circuit.

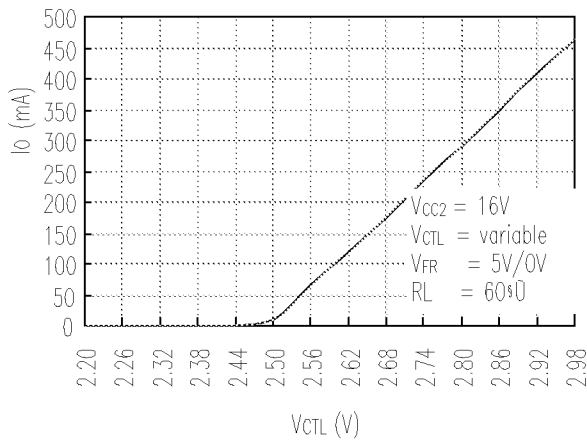
CHARACTERISTIC GRAPHS



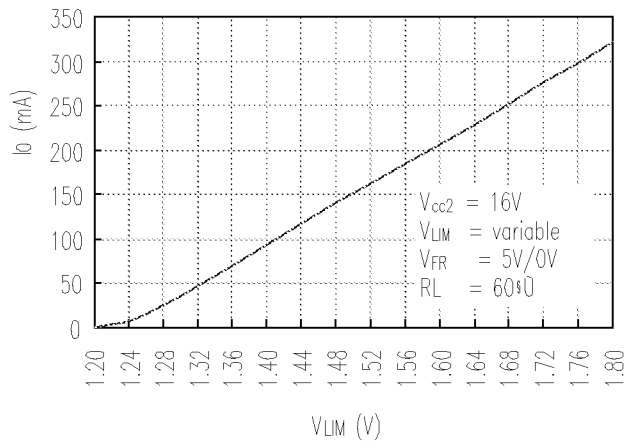
1.  $V_{CC1}$  vs  $I_{CC2}$



2.  $V_{CC2}$  vs  $I_O$

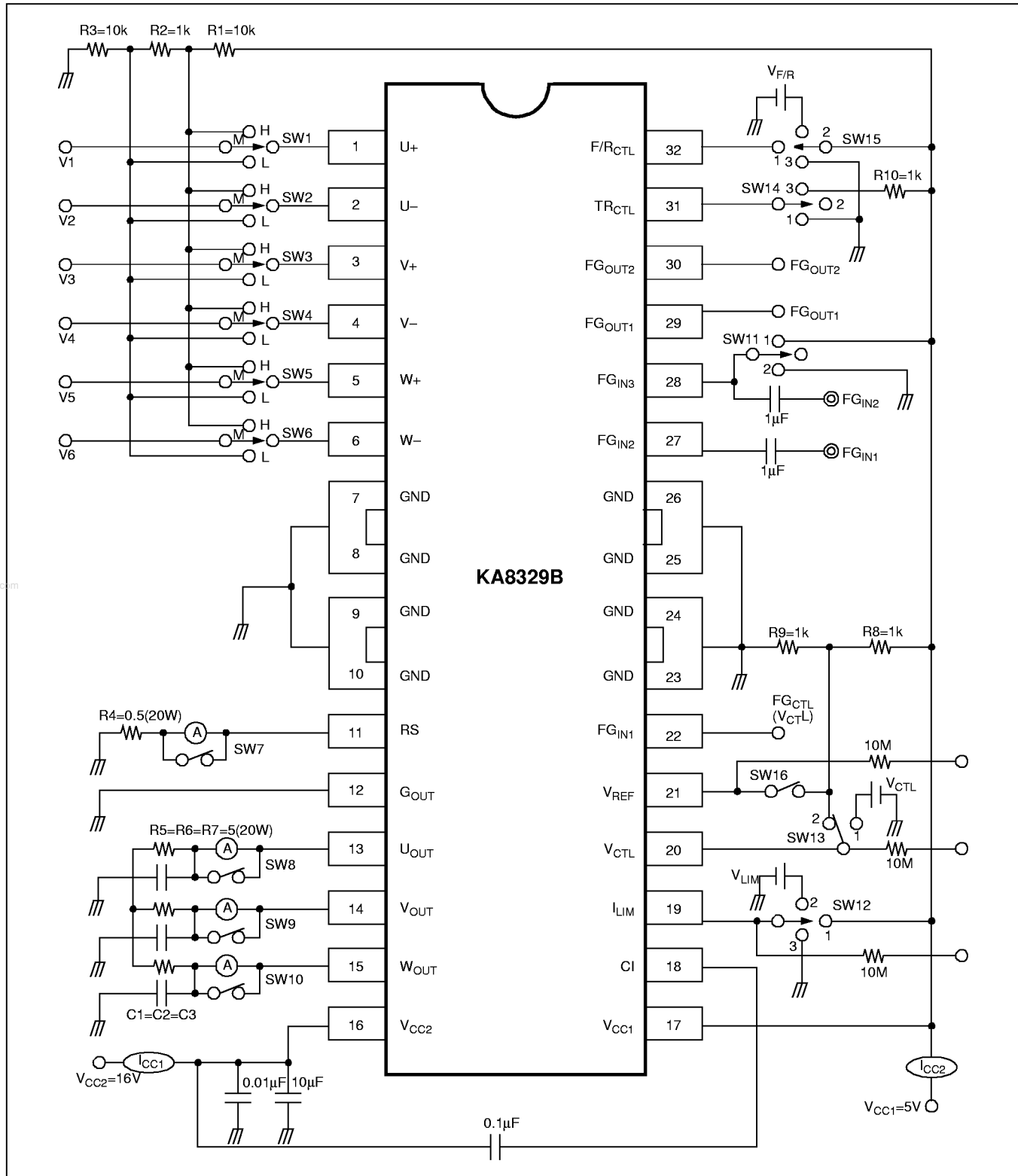


3.  $V_{CTL}$  vs  $I_O$

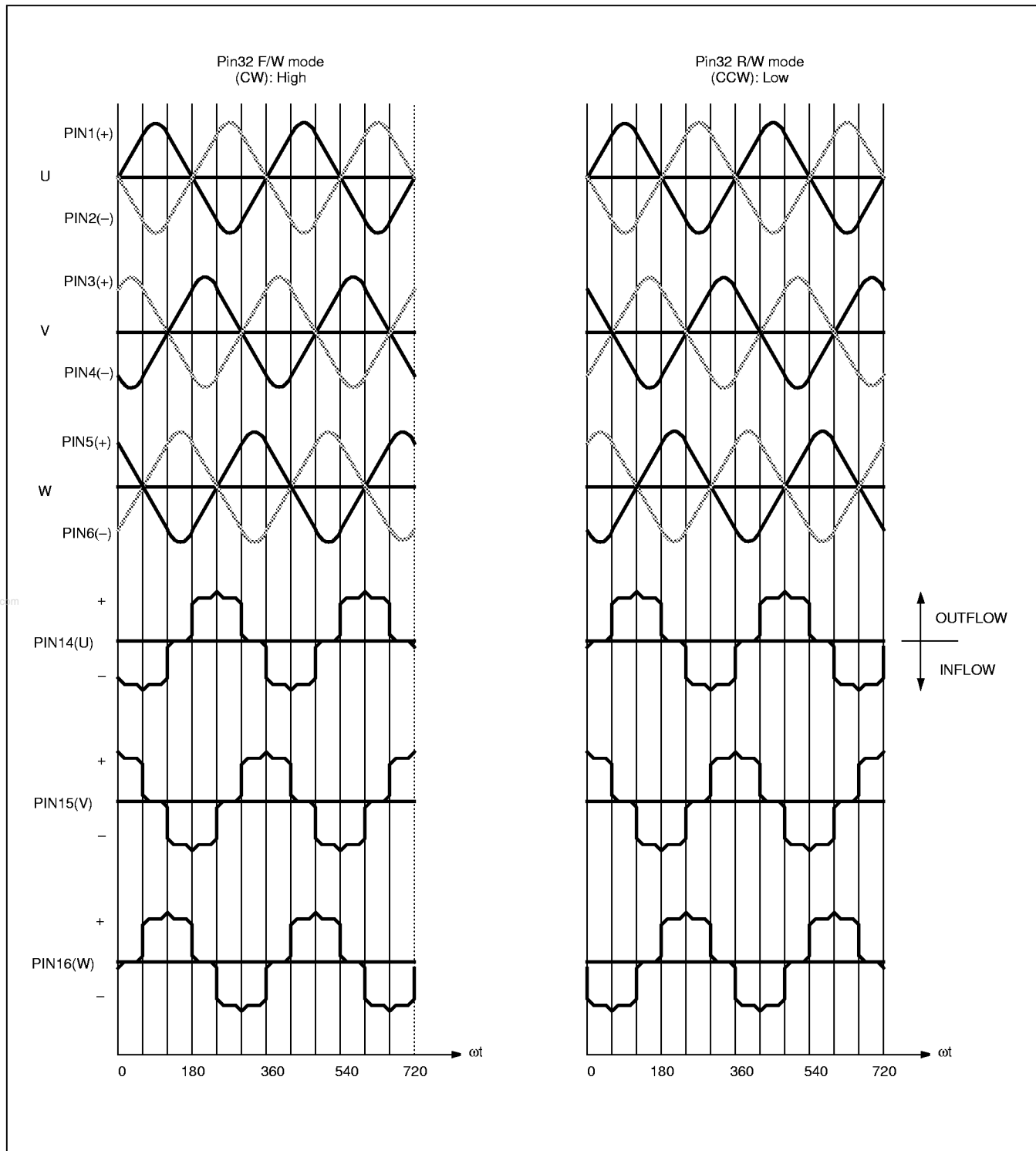


4.  $V_{LIM}$  vs  $I_O$

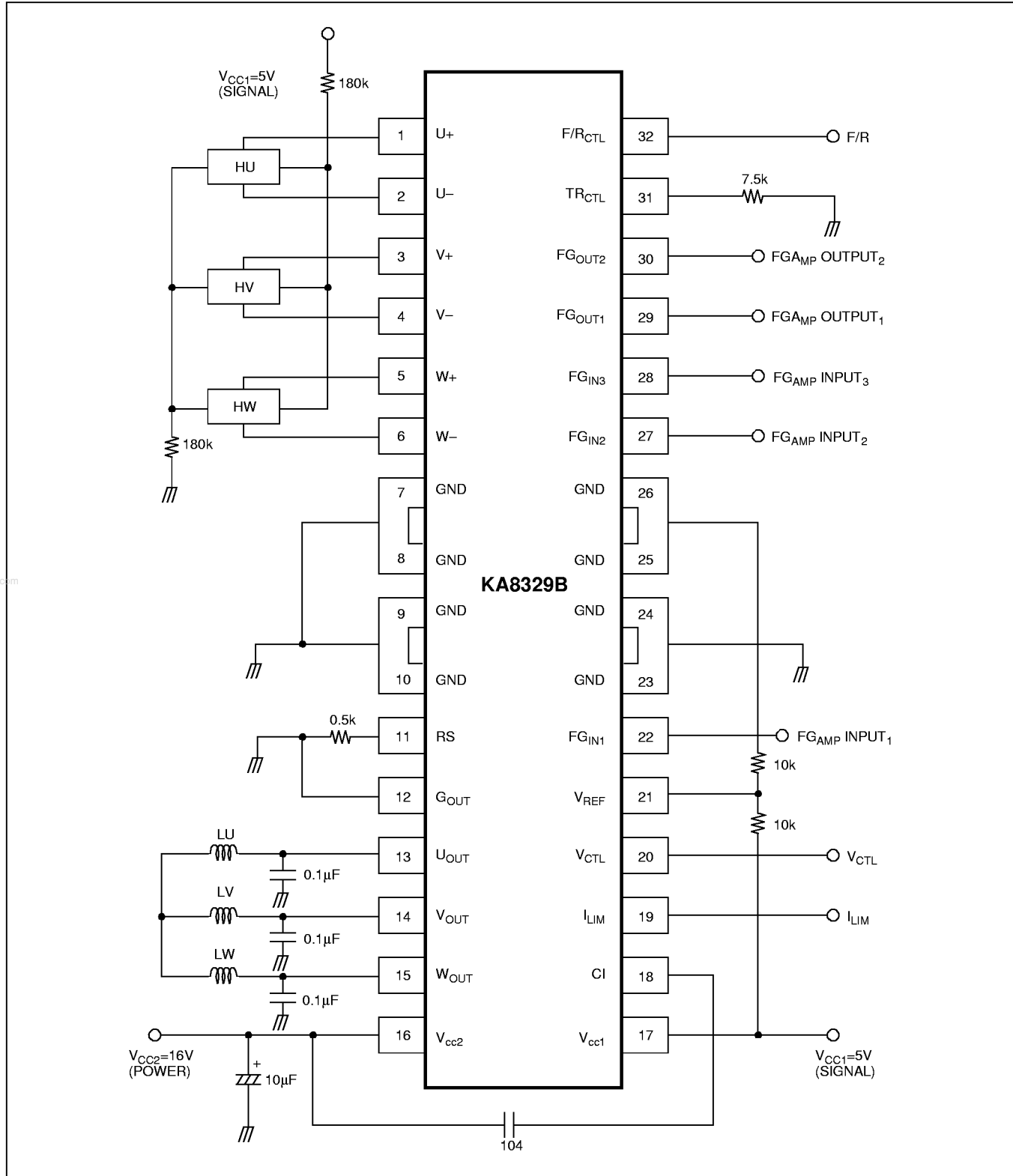
TEST CIRCUIT



TIMMING CHART



APPLICATION CIRCUIT



**NOTES**

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